

## **Appendix H   Programming With the Super Serial Card 276**

Table H-1	Baud rate selections 279
Table H-2	Data format selections 279
Table H-3	Parity selections 279
Table H-4	Time delay selections 280
Table H-5	Lowercase character display options 282
Table H-6	STSBYTE bit definitions 285
Table H-7	Error codes and bits 286
Table H-8	Memory use map 287
Table H-9	Zero-page locations used by the SSC 288
Table H-10	Address register bits interpretation 288
Table H-11	Scratchpad RAM locations used by the SSC 290

## **Appendix I   International Versions 292**

Figure I-1	International Iie schematic diagram 293
Figure I-2	English keyboard 297
Figure I-3	French keyboard 298
Figure I-4	Canadian keyboard 299
Figure I-5	German keyboard 300
Figure I-6	Italian keyboard 301
Figure I-7	Western Spanish keyboard 302
Figure I-8	Swedish keyboard 303
Table I-1	English keyboard ASCII codes 297
Table I-2	French keyboard ASCII codes 298
Table I-3	Canadian keyboard ASCII codes 299
Table I-4	German keyboard ASCII codes 300
Table I-5	Italian keyboard ASCII codes 301
Table I-6	Western Spanish keyboard ASCII codes 302
Table I-7	Swedish keyboard ASCII codes 303
Table I-8	International power supply specifications 305