

C3C4:	216 *	IN CASE WE DO A SWAP:	C401:38	7	sec		;C=1 if internal slot space	
C3C4:	217 *		C402:30 01	8	bmi	irqntcx		
C3C4:AD ED 03	218	LDA \$03ED ;GET XFERADDR LO	C404:18	9	clc			
C3C7:48	219	PHA ;SAVE ON CURRENT STACK	C405:48	10	irqntcx pha		;Save A on stack instead of \$45	
C3C8:AD EE 03	220	LDA \$03EE ;GET XFERADDR HI	C406:48	11	pha		;Make room for rts if needed	
C3CB:48	221	PHA ;SAVE IT TOO	C407:48	12	pha			
C3CC:	222 *		C408:8A	13	txa		;Save X	
C3CC:	223 *	SWITCH TO APPROPRIATE BANK:	C409:BA	14	tsx		;Get stack pointer for BRK bit	
C3CC:	224 *		C40A:E8	15	inx		;Can't do add cause we need C	
C3CC:90 08 C3D6	225	BCC XFERC2M ;=>CARD-->MAIN	C40B:E8	16	inx			
C3CE:8D 03 C0	226	STA RDCARDRAM ;SET FOR RUNNING	C40C:E8	17	inx			
C3D1:8D 05 C0	227	STA WRCARDRAM ;IN CARD RAM	C40D:E8	18	inx			
C3D4:80 06 C3DC	228	BCC XFERZP ;=> always taken	C40E:48	19	pha			
C3D6:	229	XFERC2M EQU *	C40F:98	20	tya		;and Y	
C3D6:8D 02 C0	230	STA RDMAINRAM ;SET FOR RUNNING	C410:48	21	pha			
C3D9:8D 04 C0	231	STA WRMAINRAM ;IN MAIN RAM	C411:BD 00 01	22	lda	\$100,x	;Get status for break test	
C3DC:	232 *		C414:29 10	23	and	#\$10	;A = \$10 if break	
C3DC:	233	XFERZP EQU *	C416:A8	24	tay		;Save it for later	
C3DC:68	234	PLA ;SWITCH TO ALT ZP/STX	C417:	25	* Now test &		set the state of the machine. Don't alter Y	
C3DD:8D EE 03	235	STA \$03EE ;HI AND	C417:AD 18 C0	26	lda	rd80col	;Test for 80 store and page 2	
C3E0:68	236	PLA	C41A:2D 1C C0	27	and	rdpage2		
C3E1:8D ED 03	237	STA \$03ED ; LO	C41D:29 80	28	and	#\$80	;Make it 0 or \$80	
C3E4:68	238	PLA ;RESTORE AC	C41F:P0 05 C426	29	beq	irq2	;Branch if no change needed	
C3E5:70 05 C3EC	239	BVS XFERAZP ;=>switch in alternate zp	C421:A9 20	30	lda	#\$20	;Set shifted page 2 reset bit	
C3E7:8D 08 C0	240	STA SETSTDZP ;else force standard zp	C423:8D 54 C0	31	sta	txtpage1	;Set page 1	
C3EA:50 03 C3EF	241	BVC JMPDEST ;=>always perform transfer	C426:2A	32	irq2	rol A	;Align bit & shift in slotcx bit	
C3EC:8D 09 C0	242	XFERAZP STA SETALTZP ;switch in alternate zp	C427:2C 13 C0	33	bit	rdramrd	;Are we reading from aux ram?	
C3EF:6C ED 03	243	JMPDEST JMP (\$03ED) ;=>off we go	C42A:10 05 C431	34	bpl	irq3	;Branch if main ram read	
C3F2:	244 *		C42C:8D 02 C0	35	sta	rdmainram	;Else, switch main in	
C3F2:	245	DS C3ORG+\$F4-\$*,0 ;pad to interrupt stuff	C42F:09 20	36	ora	#\$20	;and record the event	
C3F4:	246 *		C431:2C 14 C0	37	irq3	bit	rdramwrt	;Do the same for ram write
C3F4:	247 *	This is where the interrupt routine returns to.	C434:10 05 C43B	38	bpl	irq4		
C3F4:	248 *	At this point the ROM is not necessarily switched in so...	C436:8D 04 C0	39	sta	wrmainram		
C3F4:	249 *		C439:09 10	40	ora	#\$10		
C3F4:8D 81 C0	250	IRQDONE STA \$C081 ;read ROM, write RAM	C43B:	41	irq4	equ *		
C3F7:4C 7A FC	251	JMP IRQDONE2 ;and jump to ROM	C43B:2C 12 C0	42	irq5	bit	rdlclram	;Determine if language card active
C3FA:	252 *		C43E:10 0C C44C	43	bpl	irq7		
C3FA:	253 *	This is the main entry point for the interrupt	C440:09 0C	44	ora	#\$0C	;Sets two bits. Second is redundant	
C3FA:	254 *	handler. This switches in the internal ROM and	C442:2C 11 C0	45	bit	rdlcnk2	;if INC used to restore.	
C3FA:	255 *	jumps to the main part of the interrupt handler	C445:10 02 C449	46	bpl	irq6	;Branch if not page 2 of \$D000	
C3FA:	256 *	at \$C400.	C447:49 06	47	eor	#\$06	;Set bits for page 2	
C3FA:	257 *		C449:8D 81 C0	48	irq6	sta	romin	;Enable ROM STA leaves write enable alone
C3FA:2C 15 C0	258	irq bit rdcxrom ;Test internal or external rom	C44C:2C 16 C0	49	irq7	bit	rdaltzp	;Last...and very important
C3FD:8D 07 C0	259	sta setintcxrom ;Force in ROM to get to interrupt handler	C44F:10 0D C45E	50	bpl	irq8	;If alternate stack	
C400:	260 *		C451:BA	51	tsx		;store current stack pointer at \$101	
C400:	261 *	Fall into \$C400 which is now switched in!!	C452:8E 01 01	52	stx	\$101		
C400:	262 *		C455:AE 00 01	53	ldx	\$100	;Retrive main stack pointer from \$100	
C400:	20	INCLUDE IRQ	C458:9A	54	txs			
C400:	1 *		C459:8D 08 C0	55	sta	setstdzp		
C400:	2 *	Here is the main interrupt handler	C45C:09 80	56	ora	#\$80	;Mark stack switched	
C400:	3 *		C45E:88	57	dev		;Was it a break?	
C400:	4	*****	C45F:30 0C C46D	58	bmi	irq9		
C400:	5	newirq equ *	C461:85 44	59	sta	macstat	;Save state of machine	
C400:D8	6	cld ;make no assumptions!!	C463:68	60	pla		;Restore registers	