

Table F-15*
I/O memory switches

Name	Function	Location		Notes
		Hex	Decimal	
SLOT3ROM	Slot ROM at \$C300	\$C00B	49163 -16373	Write
	Internal ROM at \$C300	\$C00A	49162 -16374	Write
	Read SLOT3ROM switch	\$C017	49175 -16361	Read
SLOTXROM	Slot ROM at \$Cx00	\$C006	49159 -16377	Write
	Internal ROM at \$Cx00	\$C007	49158 -16378	Write
	Read SLOTXROM switch	\$C015	49173 -16363	Read

* Table 6-5

Table F-16*
I/O routine offsets and registers under Pascal 1.1 protocol

Address	Offset for	X register	Y register	A register
\$Cs0D	Initialization			
	On entry	\$Cs	\$s0	
	On exit	Error code	(unchanged)	(unchanged)
\$Cs0E	Read			
	On entry	\$Cs	\$s0	
	On exit	Error code	(unchanged)	Character read
\$Cs0F	Write			
	On entry	\$Cs	\$s0	Char. to write
	On exit	Error code	(unchanged)	(unchanged)
\$Cs10	Status			
	On entry	\$Cs	\$s0	Request (0 or 1)
	On exit	Error code	(changed)	(unchanged)

* Table 6-7