

Apple 2 Technical Manual

Apple 2 Programmer's Aid # 1

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Apple 2 Technical Manual •

Programmer's Aid # 1

1978

PROGRAMMER'S AID #1

INSTALLATION AND OPERATING MANUAL



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PROGRAMMER'S AID #1

INSTALLATION AND OPERATING MANUAL



Apple Utility Programs

DAVID T. CRAIG

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INTRODUCTION

FEATURES OF PROGRAMMER'S AID #1

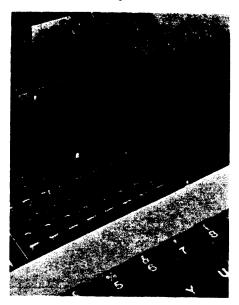
Programmer's Aid #1 combines several APPLE II programs that Integer BASIC programmers need quite frequently. To avoid having to load them from a cassette tape or diskette each time they are used, these programs have been combined in a special read-only memory (ROM) integrated circuit (IC). When this circuit is plugged into one of the empty sockets left on the APPLE's printed-circuit board for this purpose, these programs become a built-in part of the computer the same way Integer BASIC and the Monitor routines are built in. Programmer's Aid #1 allows you to do the following, on your APPLE II:

- Chapter 1. Renumber an entire Integer BASIC program, or a portion of the program.
- Chapter 2. Load an Integer BASIC program from tape without erasing the Integer BASIC program that was already in memory, in order to combine the two programs.
- Chapter 3. Verify that an Integer BASIC program has been saved correctly on tape, <u>before</u> the program is deleted from APPLE's memory.
- Chapter 4. Verify that a machine-language program or data area has been saved correctly on tape from the Monitor.
- Chapter 5. Relocate 65\(\text{02} \) machine-language programs.
- Chapter 6. Test the memory of the APPLE.
- Chapter 7. Generate musical notes of variable duration over four chromatic octaves, in five (slightly) different timbres, from Integer BASIC.
- Chapter 8. Do convenient High-Resolution graphics from Integer BASIC.

Note: if your APPLE has the firmware APPLESOFT card installed, its switch must be down (in the Integer BASIC position) for Programmer's Aid #1 to operate.

HOW TO INSTALL THE PROGRAMMER'S AID ROM

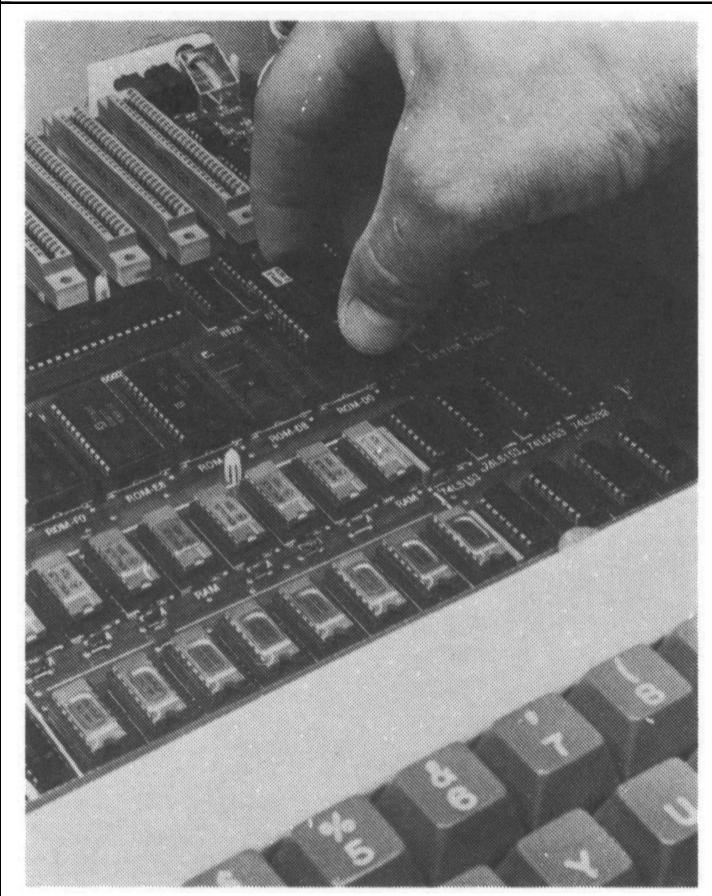
The Programmer's Aid ROM is an IC that has to be plugged into a socket on the inside of the APPLE II computer.



- 1. Turn off the power switch on the back of the APPLE II. This is important to prevent damage to the computer.
- 2. Remove the cover from the APPLE II. This is done by pulling up on the cover at the rear edge until the two corner fasteners pop apart. Do not continue to lift the rear edge, but slide cover backward until it comes free.
- 3. Inside the APPLE, toward the right center of the main printed-circuit board, locate the large empty socket in Row F, marked "ROM-DØ".
- 4. Make sure that the Programmer's Aid ROM IC is oriented correctly. The small semicircular notch should be toward the keyboard. The Programmer's Aid ROM IC must match the orientation of the other ROM ICs that are already installed in that row.
- 5. Align all the pins on the Programmer's Aid ROM IC with the holes in socket DØ, and gently press the IC into place. If a pin bends, remove the IC from its socket using an "IC puller" (or, less optimally, by prying up gently with a screwdriver). Do not attempt to pull the socket off the board. Straighten any bent pins with a needlenose pliers, and press the IC into its socket again, even more carefully.
- 6. Replace the cover of the APPLE, remembering to start by sliding the front edge of the cover into position. Press down on the two rear corners until they pop into place.
- 7. Programmer's Aid #1 is installed; the APPLE II may now be turned on.

XII

"A2MAN 030-0026-01 2-12xii.PICT" 292 KB 2001-07-22 dpi: 600h x 600v pix: 2696h x 4022v



"A2MAN 030-0026-01 2-12xii P.PICT" 6016 KB 2001-07-22 dpi: 1200
h ${\bf x}$ 1200v pix: 2195
h ${\bf x}$ 2809v

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CHAPTER PRENUMBER

- 2 Renumbering an entire BASIC program
- 2 Renumbering a portion of a BASIC program
- 4 Comments

1

RENUMBERING AN ENTIRE BASIC PROGRAM

After loading your program into the APPLE, type the

CLR

command. This clears the BASIC variable table, so that the Renumber feature's parameters will be the <u>first</u> variables in the table. The Renumber feature looks for its parameters by <u>location</u> in the variable table. For the parameters to appear in the table in their correct locations, they must be specified in the correct <u>order</u> and they must have names of the correct length.

Now, choose the number you wish assigned to the first line in your renumbered program. Suppose you want your renumbered program to start at line number 1000. Type

START = 1000

Any valid variable name will do, but it must have the correct number of characters. Next choose the amount by which you want succeeding line numbers to increase. For example, to renumber in increments of 10, type

STEP = 10

Finally, type the this command:

CALL -1Ø531

As each line of the program is renumbered, its old line number is displayed with an "arrow" pointing to the new line number. A possible example might appear like this on the APPLE's screen:

7->1000 213->1010 527->1020 698->1030 13000->1040 13233->1050

RENUMBERING PORTIONS OF A PROGRAM

You do not have to renumber your entire program. You can renumber just the lines numbered from, say, 300 to 500 by assigning values to four variables. Again, you must first type the command

CLR

to clear the BASIC variable table.

2

"A2MAN 030-0026-01 3 02.PICT" 250 KB 2001-07-22 dpi: 600h x 600v pix: 2733h x 3986v

The first two variables for partial renumbering are the same as those for renumbering the whole program. They specify that the program portion, after renumbering, will begin with line number 200, say, and that each line's number thereafter will be 20 greater than the previous line's:

START = 200 STEP = 20

The next two variables specify the program portion's range of line numbers before renumbering:

FROM = 300TO = 500

The final command is also different. For renumbering a <u>portion</u> of a program, use the command:

CALL -1Ø521

If the program was previously numbered

1ØØ

12Ø

3ØØ

31Ø

402

5ØØ

2ØØØ 2Ø22

then after the renumbering specified above, the APPLE will show this list of changes:

300->200

310->220

402->240

5ØØ->26Ø

and the new program line numbers will be

100

120

2ØØ

220

24**Ø**

26Ø 2ØØØ

2022

You cannot renumber in such a way that the renumbered lines would replace, be inserted between or be intermixed with un-renumbered lines. Thus, you cannot change the <u>order</u> of the program lines. If you try, the message

*** RANGE ERR

is displayed after the list of proposed line changes, and the line numbers themselves are left unchanged. If you type the commands in the wrong order, nothing happens, usually.

COMMENTS:

- 1. If you do not CLR before renumbering, unexpected line numbers may result. It may or may not be possible to renumber the program again and save your work.
- 2. If you omit the START or STEP values, the computer will choose them unpredictably. This may result in loss of the program.
- 3. If an arithmetic expression or variable is used in a GOTO or GOSUB, that GOTO or GOSUB will generally not be renumbered correctly. For example, GOTO TEST or GOSUB 10+20 will not be renumbered correctly.
- 4. Nonsense values for STEP, such as Ø or a negative number, can render your program unusable. A negative START value can renumber your program with line numbers above 32767, for what it's worth. Such line numbers are difficult to deal with. For example, an attempt to LIST one of them will result in a >32767 error. Line numbers greater than 32767 can be corrected by renumbering the entire program to lower line numbers.
- 5. The display of line number changes can appear correct even though the line numbers themselves have not been changed correctly. After the *** RANGE ERR message, for instance, the line numbers are left with their original numbering. LIST your program and check it before using it.
- 6. The Renumber feature applies only to Integer BASIC programs.
- 7. Occasionally, what seems to be a "reasonable" renumbering does not work. Try the renumbering again, with a different START and STEP value.

CHAPTER 2 APPEND

- 6 Appending one BASIC program to another
- 6 Comments

APPENDING ONE BASIC PROGRAM TO ANOTHER

If you have one program or program portion stored in your APPLE's memory, and another saved on tape, it is possible to combine them into one program. This feature is especially useful when a subroutine has been developed for one program, and you wish to use it in another program without retyping the subroutine.

For the Append feature to function correctly, all the line numbers of the program in memory must be <u>greater</u> than all the line numbers of the program to be appended from tape. In this discussion, we will call the program saved on tape "Programl," and the program in APPLE's memory "Program2."

If Program2 is not in APPLE's memory already, use the usual command

LOAD

to put Program2 (with high line numbers) into the APPLE. Using the Renumber feature, if necessary, make sure that all the line numbers in Program2 are greater than the highest line number in Program1.

Now place the tape for Programl in the tape recorder. Use the usual loading procedure, except that instead of the LOAD command use this command:

CALL -11076

This will give the normal beeps, and when the second beep has sounded, the two programs will both be in memory. If this step causes the message

*** MEM FULL ERR

to appear, neither Program2 nor Program1 will be accessible. In this case, use the command

CALL -11Ø59

to recover Program2, the program which was already in APPLE's memory.

COMMENTS:

- The Append feature operates only with APPLE II Integer BASIC programs.
- 2. If the line numbers of the two progams are not as described, expect unpredictable results.

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CHAPTER 3 TAPE VERIFY (BASIC)

- Verifying a BASIC program SAVEd on tape
- Comments

VERIFYING A BASIC PROGRAM SAVED ON TAPE

Normally, it is impossible (unless you have two APPLEs) to know whether or not you have successfully saved your current program on tape, in time to do something about a defective recording. The reason is this: when you SAVE a program on tape, the only way to discover whether it has been recorded correctly is to LOAD it back in to the APPLE. But, when you LOAD a program, the first thing the APPLE does is erase whatever current program is stored. So, if the tape is bad, you only find out after your current program has been lost.

The Tape Verify feature solves this problem. Save your current program in the usual way:

SAVE

Rewind the tape, and (without modifying your current program in any way) type the command

CALL -10955

Do not press the RETURN key until after you start the tape playing. If the tape reads in normally (with the usual two beeps), then it is correct. If there is any error on the tape, you will get a beep and the ERR message. If this happens, you will probably want to try re-recording the tape, although you don't know for sure whether the Tape Verify error means that the tape wasn't recorded right or if it just didn't play back properly. In any case, if it does verify, you know that it is good.

COMMENTS:

- This works only with Integer BASIC programs.
- 2. Any change in the program, however slight, between the time the program is SAVEd on tape and the time the tape is verified, will cause the verification to fail.

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CHAPTER 4 TAPE VERIFY (Machine Code or Data)

- Verifying a portion of memory saved on tape
- 10 Comments

VERIFYING A PORTION OF MEMORY SAVED ON TAPE

Users of machine-language routines will find that this version of the Tape Verify feature meets their needs. Save the desired portion of memory, from address1 to address2, in the usual way:

addressl . address2 W return

Note: the example instructions in this chapter often include spaces for easier reading; do not type these spaces.

Rewind the tape, and type (after the asterisk prompt)

D52EG return

This initializes the Tape Verify feature by preparing locations \$3F8 through \$3FA for the ctrl Y vector. Now type (do not type the spaces)

address1 . address2 ctrl Y return

and re-play the tape. The first error encountered stops the program and is reported with a beep and the word ERR. If it is not a checksum error, then the Tape Verify feature will print out the location where the tape and memory disagreed and the data that it expected on the tape.

Note: type "ctrl Y" by typing Y while holding down the CTRL key; ctrl Y is not displayed on the TV screen. Type "return" by pressing the RETURN key.

COMMENTS:

Any change in the specified memory area, however slight, between the time the program is saved on tape and the time the tape is verified, will cause the verification to fail.

CHAPTER 5 RELOCATE

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 - 12 Relocatiing machine-language code
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- 25 Part C: Further details
 - 25 Technical information
 - 26 Algorithm used by the Code-Relocation feature
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PART A: THEORY OF OPERATION

RELOCATING MACHINE-LANGUAGE CODE

Quite frequently, programmers encounter situations that call for relocating machine-language (not BASIC) programs on the 65@2-based APPLE II computer. Relocation implies creating a new version of the program, a version that runs properly in an area of memory different from that in which the original program ran.

If they rely on the relative branch instruction, certain small 6502 programs can simply be moved without alteration, using the existing Monitor Move commands. Other programs will require only minor hand-modification after Monitor Moving. These modifications are simplified on the APPLE II by the built-in disassembler, which pinpoints absolute memory-reference instructions such as JMP's and JSR's.

However, sometimes it is necessary to relocate lengthy programs containing multiple data segments interspersed with code. Using this Machine-Code Relocation feature can save you hours of work on such a move, with improved reliability and accuracy.

The following situations call for program relocation:

- 1. Two different programs, which were originally written to run in identical memory locations, must now reside and run in memory concurrently.
- 2. A program currently runs from ROM. In order to modify its operation experimentally, a version must be generated which runs from a different set of addresses in RAM.
- 3. A program currently running in RAM must be converted to run from EPROM or ROM addresses.
- 4. A program currently running on a 16K machine must be relocated in order to run on a 4K machine. Furthermore, the relocation may have to be performed on the smaller machine.
- 5. Because of memory-mapping differences, a program that ran on an APPLE I (or other 6502-based computer) falls into unusable address space on an APPLE II.
- 6. Because different operating systems assign variables differently, either page-zero or non-page-zero variable allocation for a specific program may have to modified when moving the program from one make of computer to another.

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- 7. A program, which exists as several chunks strewn about memory, must be combined in a single, contiguous block.
- 8. A program has outgrown the available memory space and must be relocated to a larger, "free" memory space.
- 9. A program insertion or deletion requires a portion of the program to move a few bytes up or down.
- 10. On a whim, the user wishes to move a program.

PROGRAM MODEL

Here is one simple way to visualize program relocation: starting with a program which resides and runs in a "Source Block" of memory, relocation creates a modified version of that program which resides and runs properly in a "Destination Block" of memory.

However, this model does not sufficiently describe situations where the "Source Block" and the "Destination Block" are the same locations in memory. For example, a program written to begin at location \$400 on an APPLE I (the \$ indicates a hexadecimal number) falls in the APPLE II screen-memory range. It must be loaded to some other area of memory in the APPLE II. But the program will not run properly in its new memory locations, because various absolute memory references, etc., are now wrong. This program can then be "relocated" right back into the same new memory locations, a process which modifies it to run properly in its new location.

A more versatile program model is as follows. A program or section of a program written to run in a memory range termed the "Source Block" actually resides currently in a range termed the "Source Segments". Thus a program written to run from location \$400 may currently reside beginning at location \$800. After relocation, the new version of the program must be written to run correctly in a range termed the "Destination Block" although it will actually reside currently in a range termed the "Destination Segments". Thus a program may be relocated such that it will run correctly from location \$D800 (a ROM address) yet reside beginning at location \$C00 prior to being saved on tape or used to burn EPROMs (obviously, the relocated program cannot immediately reside at locations reserved for ROM). In some cases, the Source and Destination Segments may overlap.

BLOCKS AND SEGMENTS EXAMPLE

Segments: Blocks: Locations where Locations in APPLE II where Programs Reside Programs Run During Relocation \$800 ---Original program runs from location (Source) \$400 on APPLE I \$B87 ——— Relocation \$CØØ -Relocated version runs from location (Destination) \$D800 on APPLE II \$F87 ----

SOURCE BLOCK: \$400-\$787 DESTINATION BLOCK: \$D800-\$DB87

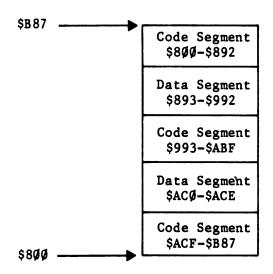
SOURCE SEGMENTS: \$800-\$887 DESTINATION SEGMENTS: \$C00-\$F87

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DATA SEGMENTS

The problem with relocating a large program all at once is that blocks of data (tables, text, etc.) may be interspersed throughout the code. During relocation, this data may be treated as if it were code, causing the data to be changed or causing code to be altered incorrectly because of boundary uncertainties introduced when the data takes on the multi-byte attribute of code. This problem is circumvented by dividing the program into code segments and data segments, and then treating the two types of segment differently.

CODE AND DATA SEGMENTS EXAMPLE



The Source <u>Code</u> Segments are <u>relocated</u> (using the 6502 Code-Relocation feature), while the Source <u>Data</u> Segments are <u>moved</u> (using the Monitor Move command).

HOW TO USE THE CODE-RELOCATION FEATURE

1. To initialize the 6502 Code-Relocation feature, press the RESET key to invoke the Monitor, and then type

D4D5G return

The Monitor user function ctrl Y will now call the Code-Relocation feature as a subroutine at location \$3F8.

Note: To type "ctrl Y", type Y while holding down the CTRL key. To type "return", press the RETURN key. In the remainder of this discussion, all instructions are typed to the right of the Monitor prompt character (*). The example instructions in this chapter often include spaces for easier reading; do not type these spaces.

- 2. Load the source program into the "Source Segments" area of memory (if it is not already there). Note that this need not be where the program normally runs.
- 3. Specify the Destination and Source <u>Block</u> parameters. Remember that a <u>Block</u> refers to locations from which the program will <u>run</u>, <u>not</u> the locations at which the Source and Destination <u>Segments</u> actually <u>reside</u> during the relocation. If only a portion of a program is to be relocated, then that portion alone is specified as the Block.

DEST BLOCK BEG < SOURCE BLOCK BEG . SOURCE BLOCK END ctrl Y * return

Notes: the syntax of this command closely resembles that of the Monitor Move command. Type "ctrl Y" by pressing the Y key while holding down the CTRL key. Then type an asterisk (*); and finally, type "return" by pressing the RETURN key. Do not type any spaces within the command.

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4. Move all Data Segments and relocate all Code Segments in sequential (increasing address) order. It is wise to prepare a list of segments, specifying beginning and ending addresses, and whether each segment is code or data.

If First Segment is Code:

DEST SEGMENT BEG < SOURCE SEGMENT BEG . SOURCE SEGMENT END ctrl Y return

If First Segment is Data:

DEST SEGMENT BEG < SOURCE SEGMENT BEG . SOURCE SEGMENT END M return

After the first segment has been either relocated (if Code) or Moved (if data), subsequent segments can be relocated or Moved using a shortened form of the command.

Subsequent Code Segments:

. SOURCE SEGMENT END ctrl Y return

(Relocation)

Subsequent Data Segments:

. SOURCE SEGMENT END M return

(Move)

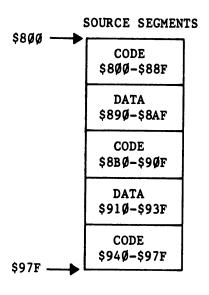
Note: the shortened form of the command can only be used if each "subsequent" segment is contiguous to the segment previously relocated or Moved. If a "subsequent" segment is in a part of memory that does not begin exactly where the previous segment ended, it must be Moved or relocated using the full "First Segment" format.

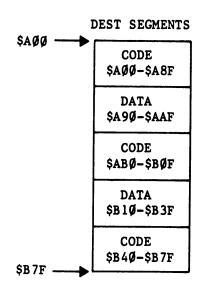
If the relocation is performed "in place" (SOURCE and DEST SEGMENTs reside in identical locations) then the SOURCE SEGMENT BEG parameter may be omitted from the First Segment relocate or Move command.

PART B: CODE-RELOCATION EXAMPLES

EXAMPLE 1. Straightforward Relocation

Program A resides and runs in locations \$800-\$97F. The relocated version will reside and run in locations \$A00-\$B7F.





SOURCE BLOCK: \$800-\$97F SOURCE SEGMENTS: \$800-\$97F

DEST BLOCK: \$AØØ-\$B7F DEST SEGMENTS: \$AØØ-\$B7F

(a) Initialize Code-Relocation feature:

reset D4D5G return

(b) Specify Destination and Source Block parameters (locations from which the program will run):

AØØ < 8ØØ . 97F ctrl Y * return

(c) Relocate first segment (code):

AØØ < 8ØØ . 88F ctrl Y return

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(d) Move subsequent Data Segments and relocate subsequent Code Segments, in ascending address sequence:

	8AF	M return	(data)
	9ØF	ctrl Y return	(code)
-		M return	(data)
		ctrl Y return	(code)

Note that step (d) illustrates abbreviated versions of the following commands:

A90	<	890	•	8AF	M return	(data)
ABØ	<	8BØ		9 0 F	ctrl Y return	(code)
-					M return	(data)
B4Ø	<	940		97F	ctrl Y return	(code)

EXAMPLE 2. Index into Block

Suppose that the program of Example 1 uses an indexed reference into the Data Segment at \$890 as follows:

LDA 7BØ, X

where the X-REG is presumed to contain a number in the range \$EØ to \$FF. Because address \$7BØ is outside the Source Block, it will not be relocated. This may be handled in one of two ways.

- (a) You may fix the exception by hand; or
- (b) You may begin the Block specifications one page lower than the addresses at which the original and relocated programs begin to use all such "early references." One lower page is enough, since FF (the number of bytes in one page) is the largest offset number that the X-REG can contain. In EXAMPLE 1, change step (b) to:

900 < 700 . 97F ctrl Y * return

Note: with this Block specification, all program references to the "prior page" (in this case the \$700 page) will be relocated.

EXAMPLE 3. Immediate Address References

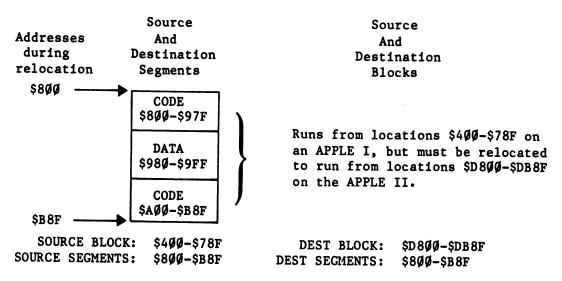
Suppose that the program of EXAMPLE 1 has an immediate reference which is an address. For example,

LDA #\$3F STA LOCØ LDA #\$Ø8 STA LOC1 JMP (LOCØ)

In this example, the LDA #\$08 will not be changed during relocation and the user will have to hand-modify it to \$04.

EXAMPLE 4. Unusable Block Ranges

Suppose a program was written to run from locations \$400-\$78F on an APPLE I. A version which will run in ROM locations \$D800-\$D88F must be generated. The Source (and Destination) Segments will reside in locations \$800-\$B8F on the APPLE II during relocation.



(a) Initialize the Code-Relocation feature:

reset D4D5G return

(b) Load original program into locations \$800-\$B8F (despite the fact that it doesn't run there):

800 . B8F R return

(c) Specify Destination and Source Block parameters (locations from which the original and relocated versions will run):

D800 < 400 . 78F ctrl Y return

(d) Move Data Segments and relocate Code Segments, in ascending address sequence:

8	ØØ <	8ØØ	•	97F	ctrl	Y	return	(first segment,	code)
	9FF	M	r	etur	n			(data)	
	B8F	ct	r1	Y	return			(code)	

Note that because the relocation is done "in place", the SOURCE SEGMENT BEG parameter is the same as the DEST SEGMENT BEG parameter (\$800) and need not be specified. The initial segment relocation command may be abbreviated as follows:

8ØØ < . 97F ctrl Y return

EXAMPLE 5. Changing the Page Zero Variable Allocation

Suppose the program of EXAMPLE 1 need not be relocated, but the page zero variable allocation is from \$20 to \$3F. Because these locations are reserved for the APPLE II system monitor, the allocation must be changed to locations \$80-\$9F. The Source and Destination Blocks are thus not the program but rather the variable area.

SOURCE BLOCK: \$2**9**-\$3F DEST BLOCK: \$8**9**-\$9F SOURCE SEGMENTS: \$8**99**-\$97F DEST SEGMENTS: \$8**99**-\$97F

(a) Initialize the Code-Relocation feature:

reset D4D5G return

(b) Specify Destination and Source Blocks:

 $8\emptyset < 2\emptyset$. 3F ctrl Y * return

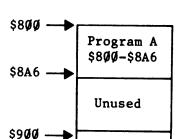
(c) Relocate Code Segments and Move Data Segments, in place:

8	ØØ <	• 88F ctrl Y return	(first segment, code)
	8AF	M return	(data)
•	9ØF	ctrl Y return	(code)
•	93F	M return	(data)
•	97F	ctrl Y return	(code)

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EXAMPLE 6. Split Blocks with Cross-Referencing

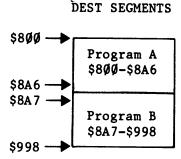
Program A resides and runs in locations \$800-\$8A6. Program B resides and runs in locations \$900-\$9Fl. A single, contiguous program is to be generated by moving Program B so that it immediately follows Program A. Each of the programs contains references to memory locations within the other. It is assumed that the programs contain no Data Segments.



\$9F1 -

SOURCE SEGMENTS

Program B \$900-\$9F1



SOURCE BLOCK: \$900-\$9F1 DEST BLOCK: \$8A7-\$998 SOURCE SEGMENTS: \$800-\$8A6 (A) DEST SEGMENTS: \$800-\$8A6 (A) \$900-\$9F1 (B) \$8A7-\$998 (B)

(a) Initialize the Code-Relocation feature:

D4D5G return

(b) Specify Destination and Source Blocks (Program B only):

8A7 < 900 . 9F1 ctrl Y * return

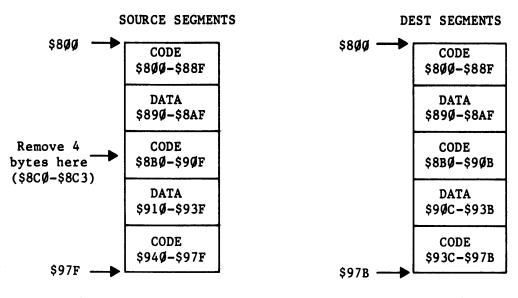
(c) Relocate each of the two programs individually. Program A must be relocated even though it does not move.

800 < .8A6 ctrl Y return (program A, "in place") 8A7 < 900 .9Fl ctrl Y return (program B, not "in place")

Note that any Data Segments within the two programs would necessitate additional relocation and Move commands.

EXAMPLE 7. Code Deletion

Four bytes of code are to be removed from within a program, and the program is to contract accordingly.



SOURCE BLOCK: \$8C4-\$97F DEST BLOCK: \$8CØ-\$97B SOURCE SEGMENTS: DEST SEGMENTS: \$800-\$88F (code) \$800-\$88F (code) \$89Ø-\$8AF (data) \$89Ø-\$8AF (data) \$8BØ-\$8BF (code) \$8BØ-\$8BF (code) \$8C4-\$9ØF (code) \$8CØ-\$9ØB (code) \$91**Ø**-\$93F (data) \$9ØC-\$93B (data) \$94**Ø**-\$97**F** (code) \$93C-\$97B (code)

(a) Initialize Code-Relocation feature:

reset D4D5G return

(b) Specify Destination and Source Blocks:

8CØ < 8C4 . 97F ctrl Y * return

(c) Relocate Code Segments and Move Data Segments, in ascending address sequence:

```
800 < 88F ctrl Y return (first segment, code, "in place")

8AF M return (data)

8BF ctrl Y return (code)

8C0 < 8C4 · 90F ctrl Y return (data)

93F M return (data)

97F ctrl Y return (code)
```

(d) Relative branches crossing the deletion boundary will be incorrect, since the relocation process does not modify them (only zero-page and absolute memory references). The user must patch these by hand.

EXAMPLE 8. Relocating the APPLE II Monitor \$\$500-\$55 to Run in RAM \$500-\$55

SOURCE BLOCK: \$F700-\$FFFF DEST BLOCK: \$700-\$FFF

(see EXAMPLE 2)

SOURCE SEGMENTS: \$F800-\$F961 (code) DEST SEGMENTS: \$800-\$961 (code)

\$F962-\$FA42 (data) \$962-\$A42 (data) \$FA43-\$FB18 (code) \$A43-\$B18 (code) \$FB19-\$FB1D (data) \$B19-\$B1D (data) \$FB1E-\$FFCB (code) \$FFCC-\$FFF (data) \$FCC-\$FFF (data)

IMMEDIATE ADDRESS REFERENCES (see EXAMPLE 3): \$FFBF \$FEA8

(more if not relocating
 to page boundary)

(a) Initialize the Code-Relocation feature:

reset D4D5G return

(b) Specify Destination and Source Block parameters:

700 < F700 . FFFF ctrl Y * return

(c) Relocate Code Segments and move Data Segments, in ascending address sequence:

800 < F800 . F961 ctrl Y return (first segment, code)
. FA42 M return (data)
. FB18 ctrl Y return (code)
. FB1D M return (data)
. FFCB ctrl Y return (code)
. FFFF M return (data)

(d) Change immediate address references:

FBF: E return (was \$FE) EA8: E return (was \$FE)

PART C: PLOTTING POINTS AND LINES

TECHNICAL INFORMATION

The following details illustrate special technical features of the APPLE II which are used by the Code-Relocation feature.

1. The APPLE II Monitor command

Addr4 < Addrl . Addr2 ctrl Y return

(Addrl, Addr2, and Addr4 are addresses)

vectors to location \$3F8 with the value Addrl in locations \$3C (low) and \$3D (high), Addr2 in locations \$3E (low) and \$3F (high), and Addr4 in locations \$42 (low) and \$43 (high). Location \$34 (YSAV) holds an index to the next character of the command buffer (after the ctrl Y). The command buffer (IN) begins at \$200.

2. If ctrl Y is followed by *, then the Block parameters are simply preserved as follows:

Parameter	Preserved at	SWEET16 Reg Name
DEST BLOCK BEG	\$8, \$9	TOBEG
SOURCE BLOCK BEG	\$2, \$3	FRMBEG
SOURCE BLOCK END	\$4, \$5	FRMEND

- 3. If ctrl Y is not followed by *, then a segment relocation is initiated at RELOC2 (\$3BB). Throughout, Addrl (\$3C, \$3D) is the Source Segment pointer and Addr4 (\$42, \$43) is the Destination Segment pointer.
- 4. INSDS2 is an APPLE II Monitor subroutine which determines the length of a 6502 instruction, given the opcode in the A-REG, and stores that opcode's instruction length in the variable LENGTH (location \$2F).

Instruction Type	LENGTH		
in A-REG	(in \$2F)		
Invalid	ø		
l byte	Ø		
2 byte	1		
3 byte	2		

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"A2MAN 030-0026-01 3 25.PICT" 258 KB 2001-07-22 dpi: 600h x 600v pix: 2841h x 4059v

5. The code from XLATE to SW16RT (\$3D9-\$3E6) uses the APPLE II 16-bit interpretive machine, SWEET16. The target address of the 65Ø2 instruction being relocated (locations \$C low and \$D high) occupies the SWEET16 register named ADR. If ADR is between FRMBEG and FRMEND (inclusive) then it is replaced by

ADR - FRMBEG + TOBEG

6. NXTA4 is an APPLE II Monitor subroutine which increments Addrl (Source Segment index) and Addr4 (Destination Segment index). If Addrl exceeds Addr2 (Source Segment end), then the carry is set; otherwise, it is cleared.

ALGORITHM USED BY THE CODE-RELOCATION FEATURE

- Set SOURCE PTR to beginning of Source Segment and DEST PTR to beginning of Destination Segment.
- 2. Copy 3 bytes from Source Segment (using SOURCE PTR) to temp INST area.
- 3. Determine instruction length from opcode (1, 2 or 3 bytes).
- 4. If two-byte instruction with non-zero-page addressing mode (immediate or relative) then go to step 7.
- 5. If two-byte instruction then clear 3rd byte so address field is Ø-255 (zero page).
- 6. If address field (2nd and 3rd bytes of INST area) falls within Source Block, then substitute

ADR - SOURCE BLOCK BEG + DEST BLOCK BEG

- 7. Move "length" bytes from INST area to Destination Segment (using DEST PTR). Update SOURCE and DEST PTR's by length.
- 8. If SOURCE PTR is less than or equal to SOURCE SEGMENT END then goto step 2., else done.

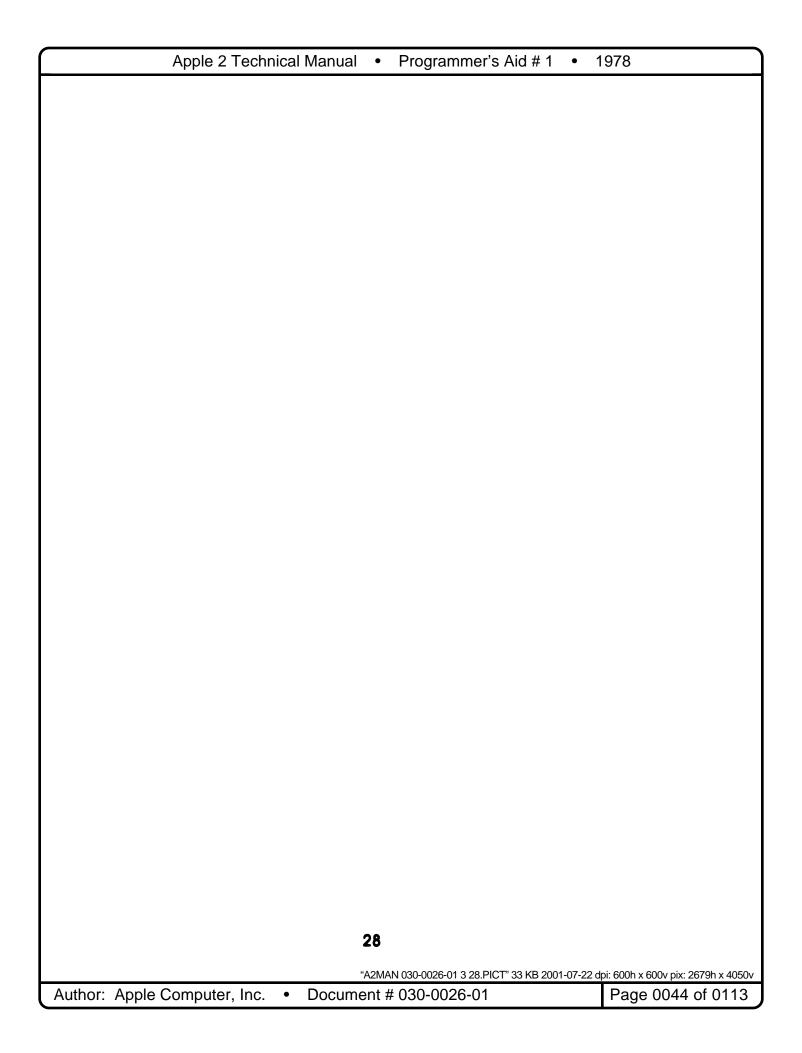
COMMENTS:

Each Move or relocation is carried out sequentially, one byte at a time, beginning with the byte at the smallest source address. As each source byte is Moved or relocated, it overwrites any information that was in the destination location. This is usually acceptable in these kinds of Moves and relocations:

- 1. Source Segments and Destination Segments do not share any common locations (no source location is overwritten).
- 2. Source Segments are in locations <u>identical</u> to the locations of the Destination Segments (each source byte overwrites itself).
- 3. Source Segments are in locations whose addresses are <u>larger</u> than the addresses of the Destination Segments' locations (any overwritten source bytes have already been Moved or relocated). This is a move toward smaller addresses.

If, however, the Source Segments and the Destination Segments share some common locations, and the Source Segments occupy locations whose addresses are <u>smaller</u> than the addresses of the Destination Segments' locations, then the source bytes occupying the common locations will be overwritten <u>before</u> they are Moved or relocated. If you attempt such a relocation, you will lose your program and data in the memory area common to both Source Segments and Destination Segments. To accomplish a small Move or relocation <u>toward larger</u> addresses, you must Move or relocate to an area of memory well away from the Source Segments (no address in common); then Move the entire relocated program back to its final resting place.

Note: the example instructions in this chapter often include spaces for easier reading; do <u>not</u> type these spaces.



CHAPTER **RAM TEST**

- 3Ø Testing APPLE's memory
- Address ranges for standard memory configurations
- 32 Error messages

Type I - Simple error Type II - Dynamic error

- Testing for intermittent failure 33
- 34 Comments

TESTING THE APPLE'S MEMORY

With this program, you can easily discover any problems in the RAM (for Random Access Memory) chips in your APPLE. This is especially useful when adding new memory. While a failure is a rare occurrence, memory chips are both quite complex and relatively expensive. This program will point out the exact memory chip or chips, if any, that have malfunctioned.

Memory chips are made in two types: one type can store 4K (4096) bits of information, the other can store 16K (16384) bits of information. Odd as it seems, the two types <u>look</u> alike, except for a code number printed on them.

The APPLE has provisions for inserting as many as 24 memory chips of either type into its main printed-circuit board, in three rows of eight sockets each. An eight-bit byte of information consists of one bit taken from each of the eight memory chips in a given row. For this reason, memory can be added only in units of eight identical memory chips at a time, filling an entire row. Eight 4K memory chips together in one row can store 4K bytes of information. Eight 16K memory chips in one row can store 16K bytes of information.

Inside the APPLE II, the three rows of sockets for memory chips are row "C", row "D" and row "E". The rows are lettered along the left edge of the printed-circuit board, as viewed from the front of the APPLE. The memory chips are installed in the third through the tenth sockets (counting from the left) of rows C, D and E. These sockets are labelled "RAM". Row C must be filled; and row E may be filled only if row D is filled. Depending on the configuration of your APPLE's memory, the eight RAM sockets in a given row of memory must be filled entirely with 4K memory chips, entirely with 16K memory chips, or all eight RAM sockets may be empty.

To test the memory chips in your computer, you must first initialize the RAM Test program. Press the RESET key to invoke the Monitor, and then type

D5BCG return

Next, specify the hexadecimal starting address for the portion of memory that you wish to test. You must also specify the hexadecimal number of "pages" of memory that you wish tested, beginning at the given starting address. A page of memory is 256 bytes (\$100 Hex). Representing the address by "a" and the number of pages by "p" (both in hexadecimal), start the RAM test by typing

a . p ctrl Y return

Note 1: to type "ctrl Y", type Y while holding down the CTRL key; ctrl Y is not displayed on the TV screen. Type "return" by pressing the RETURN key. The example instructions in this chapter often include spaces for easier reading; do not type these spaces.

Note 2: test length p*100 must not be greater than starting address a.

30

"A2MAN 030-0026-01 3 30.PICT" 423 KB 2001-07-22 dpi: 600h x 600v pix: 2814h x 4068v

For example,

2000.10 ctr1 Y return

tests hexadecimal 1000 bytes of memory (4096, or "4K" bytes, in decimal), starting at hexadecimal address 2000 (8192, or "8K", in decimal).

If the asterisk returns (after a delay that may be a half minute or so) without an error message (see ERROR MESSAGES discussion), then the specified portion of memory has tested successfully.

TABLE OF ADDRESS RANGES FOR STANDARD RAM CONFIGURATIONS

	Contains this	And the total
Then	Range of	System Memory,
Row of	Hexadecimal	If this is last
Memory	RAM Addresses	Row filled, is
С	ØØØØ-ØFFF	4K
D		8K
E	2000-2FFF	12K
С	0000-3FFF	16K
Ď	4000-4FFF	2ØK
E	5 000- 5FFF	24K
С	0000-3FFF	16K
D		32K
E	8000-BFFF	48K
	Row of Memory C D E C D C D C	Then Range of Row of Hexadecimal Memory RAM Addresses C

A 4K RAM Row contains 10 Hex pages (hex 1000 bytes, or decimal 4096 bytes). A 16K RAM Row contains 40 Hex pages (hex 4000 bytes, or decimal 16384 bytes).

A complete test for a 48K system would be as follows:

```
This tests the screen area of memory

800.8 ctrl Y return

1000.10 ctrl Y return

2000.20 ctrl Y return

4000.40 ctrl Y return

These first four tests examine

the first 16K row of memory (Row C)

4000.40 ctrl Y return

This tests the second 16K row of memory (Row D)

8000.40 ctrl Y return

This tests the shird 16K row of memory (Row E)
```

Systems containing more than 16K of memory should also receive the following special test that looks for problems at the boundary between rows of memory:

3000.20 ctrl Y return

Systems containing more than 32K of memory should receive the previous special test, plus the following:

7000.20 ctrl Y **return**

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"A2MAN 030-0026-01 3 31.PICT" 297 KB 2001-07-22 dpi: 600h x 600v pix: 2823h x 4059v

Tests may be run separately or they may be combined into one instruction. For instance, for a 48K system you can type:

400.4 ctrl Y 800.8 ctrl Y 1000.10 ctrl Y 2000.20 ctrl Y 3000.20 ctrl Y 4000.40 ctrl Y 7000.20 ctrl Y 8000.40 ctrl Y return

Remember, ctrl Y will not print on the screen, but it $\underline{\text{must}}$ be typed. With the single exception noted in the section TESTING FOR INTERMITTENT FAILURE, spaces are shown for easier reading but should $\underline{\text{not}}$ be typed.

During a full test such as the one shown above, the computer will beep at the completion of each sub-test (each sub-test ends with a ctrl Y). At the end of the full test, if no errors have been found the APPLE will beep and the blinking cursor will return with the Monitor prompt character (*). It takes approximately 50 seconds for the computer to test the RAM memory in a 16K system; larger systems will take proportionately longer.

ERROR MESSAGES

TYPE I - Simple Error

During testing, each memory address in the test range is checked by writing a particular number to it, then reading the number actually stored at that address and comparing the two.

A simple error occurs when the number written to a particular memory address differs from the number which is then read back from that <u>same</u> address. Simple errors are reported in the following format:

xxxx yy zz ERR r-c

where xxxx is the hexadecimal address at which the error was detected;

yy is the hexadecimal data written to that address;

zz is the hexadecimal data read back from that address; and

r-c is the row and column where the defective memory chip was found. Count from the left, as viewed from the front of the APPLE: the leftmost memory chip is in column 3, the rightmost is in column 10.

Example:

2Ø1F ØØ 1Ø ERR D-7

TYPE II - Dynamic Error

This type of error occurs when the act of writing a number to <u>one</u> memory address causes the number read from a <u>different</u> address to change. If no simple error is detected at a tested address, all the addresses that differ from the tested address by one bit are read for changes indicating dynamic errors. Dynamic errors are reported in the following format:

xxxx yy zz vvvv qq ERR r-c

where xxxx is the hexadecimal address at which the error was detected;

- yy is the hexadecimal data written earlier to address xxxx;
- zz is the hexadecimal data now read back from address xxxx;
- vvvv is the current hexadecimal address to which data qq was successfully written;
 - qq is the hexadecimal data successfully written to, and read back from, address vvvv; and
 - r-c is the row and column where the defective memory chip was found. Count from the left, as viewed from the front of the APPLE: the leftmost memory chip is in column 3, the rightmost is in column 10. In this type of error, the indicated row (but not the column) may be incorrect.

This is similar to Type I, except that the appearance of vvvv and qq indicates an error was detected at address xxxx after data was successfully written at address vvvv.

Example:

5Ø51 ØØ Ø8 5451 ØØ ERR E-6

After a dynamic error, the indicated row (but not the column) may be incorrect. Determine exactly which tests check each row of chips (according to the range of memory addresses corresponding to each row), and run those tests by themselves. Confirm your diagnosis by replacing the suspected memory chip with a known good memory chip (you can use either a 4K or a 16K memory chip, for this replacement). Remember to turn off the APPLE's power switch and to discharge yourself before handling the memory chips.

TESTING FOR INTERMITTENT FAILURE (Automatically Repeating Test)

This provides a way to test memory over and over again, indefinitely. You will type a complete series of tests, just as you did before, except that you will:

- a. precede the complete test with the letter N
- b. follow the complete test with $34:\emptyset$
- c. type at least one space before pressing the RETURN key.

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"A2MAN 030-0026-01 3 33.PICT" 347 KB 2001-07-22 dpi: 600h x 600v pix: 2832h x 4050v

Here is the format:

N (memory test to be repeated) 34:0 (type one space) return

NOTE: You <u>must</u> type at least one space at the end of the line, prior to pressing the RETURN key. This is the only space that should be typed (all other spaces shown within instructions in this chapter are for easier reading only; they should not be typed).

Example (for a 48K system):

N 400.4 ctrl Y 800.8 ctrl Y 1000.10 ctrl Y 2000.20 ctrl Y 3000.20 ctrl Y 4000.40 ctrl Y 7000.20 ctrl Y 8000.40 ctrl Y 34:0 return

Run this test for at least one hour (preferably overnight) with the APPLE's lid in place. This allows the system and the memory chips to reach maximum operating temperature.

Only if a failure occurs will the APPLE display an error message and rapidly beep three times; otherwise, the APPLE will beep once at the successful end of each sub-test. To stop this repeating test, you must press the RESET key.

COMMENTS:

- l. You cannot test the APPLE's memory below the address of 400 (Hex), since various pointers and other system necessities are there. In any case, if that region of memory has problems, the APPLE won't function.
- 2. For any subtest, the number of pages tested cannot be greater than the starting address divided by 100 Hex. 2000.30 ctrl Y will not work, but 5000.30 ctrl Y will.
- 3. Before changing anything inside the APPLE, make sure the APPLE is plugged into a grounded, 3-wire power outlet, and that the power switch on the back of the computer is turned off. Always touch the outside metal bottom plate of the APPLE II, prior to handling any memory chips. This is done to remove any static charge that you may have acquired.

EVEN A SMALL STATIC CHARGE CAN DESTROY MEMORY CHIPS

4. Besides the eight memory chips, some additions of memory require changing three other chip-like devices called Memory Configuration Blocks. The Memory Configuration Blocks tell the APPLE which type of memory chip (4K or 16K) is to be plugged into each row of memory. A complete package for adding memory to your computer, containing all necessary parts and detailed instructions, can be purchased from APPLE Computer Inc. To add 4K of memory, order the 4K Memory Expansion Module (P/N A2MØØ14). To add 16K of memory, order the 16K Memory Expansion Module (P/N A2MØØ16).

CHAPTER MUSIC

- 36 Generating musical tones
- 37 Comments

GENERATING MUSICAL TONES

The Music feature is most easily used from within an Integer BASIC program. It greatly simplifies the task of making the APPLE II into a music-playing device.

There are three things the computer needs to know before playing a note: pitch (how high or low a note), duration (how long a time it is to sound), and timbre. Timbre is the quality of a sound that allows you to distinguish one instrument from another even if they are playing at the same pitch and loudness. This Music feature does not permit control of loudness.

It is convenient to set up a few constants early in the program:

MUSIC = -10473

PITCH = 767 Note \$896

TIME = 766 LONG \$845

TIMBRE = 765 VOICE \$846

There are 50 notes available, numbered from 1 to 50. The statement

POKE PITCH, 32

will set up the Music feature to produce (approximately) the note middle C. Increasing the pitch value by one increases the pitch by a semitone. Thus

POKE PITCH, 33

would set up the Music feature to produce the note C sharp. Just over four chromatic octaves are available. The note number Ø indicates a rest (a silence) rather than a pitch.

The duration of the note is set by

POKE TIME, t

Where t is a number from 1 to 255. The higher the number, the longer the note. A choice of t = 170 gives notes that are approximately one second long. To get notes at a metronome marking of MM, use a duration of 10200/MM. For example, to get 204 notes per minute (approximately) use the command

POKE TIME, 10200/204

There are five timbres, coded by the numbers 2, 8, 16, 32 and 64. They are not very different from one another. With certain timbres, a few of the extremely low or high notes do not give the correct pitch. Timbre 32 does not have this problem.

POKE TIMBRE, 32

When the pitch, time, and timbre have been set, the statement

CALL MUSIC

will cause the specified note to sound.

The following program plays a chromatic scale of four octaves:

```
10 MUSIC = -10473: PITCH = 767: TIME = 766: TIMBRE = 765
```

60 NEXT I: END

Where X is a number from 51 through 255,

POKE PITCH, X

will specify various notes, in odd sequences. In the program above, change line $4\emptyset$ to

40 POKE PITCH, 86

for a demonstration.

COMMENTS:

Some extremely high or low notes will come out at the wrong pitch with certain timbres.

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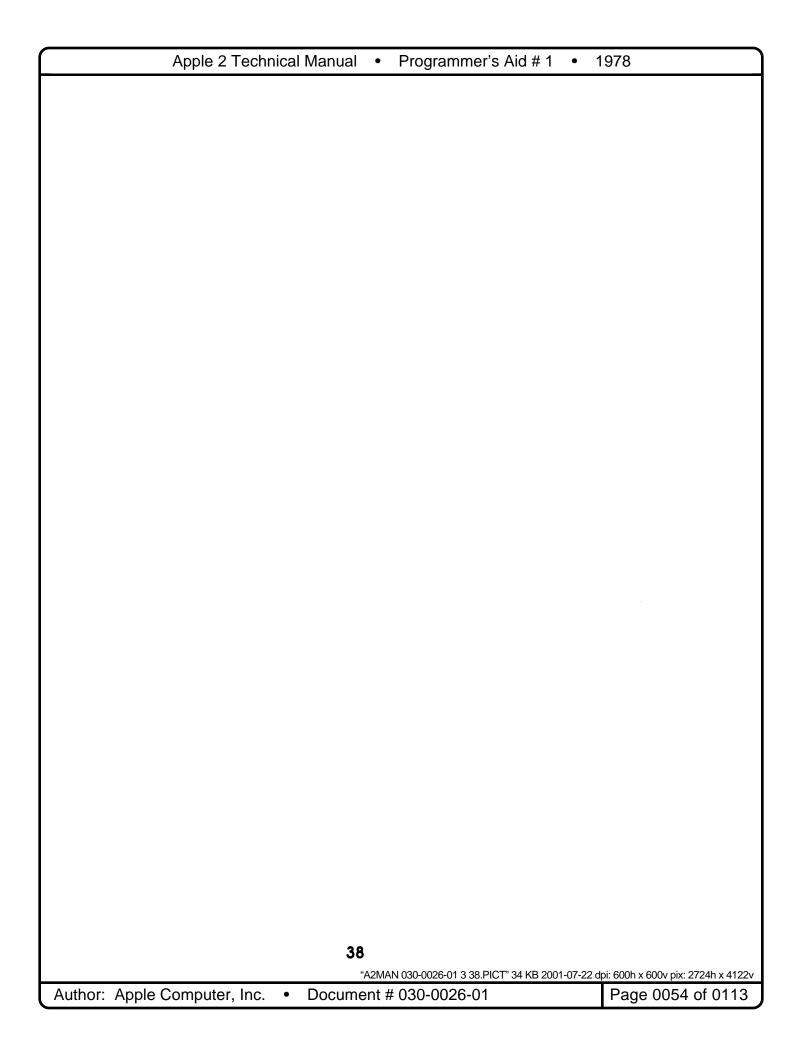
"A2MAN 030-0026-01 3 37.PICT" 163 KB 2001-07-22 dpi: 600h x 600v pix: 2832h x 4149v

²⁰ POKE TIME, 40: POKE TIMBRE, 32

 $^{3\}emptyset$ FOR I = 1 TO 49

⁴⁰ POKE PITCH, I

⁵⁰ CALL MUSIC



CHAPTER 8 **HIGH-RESOLUTION** GRAPHICS

- 40 Part A: Setting up parameters, subroutines, and colors
 - Positioning the High-Resolution parameters
 - 41 Defining subroutine names
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 - The INITialization subroutine
 - 43 Changing the graphics screen
 - 44 CLEARing the screen to black
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- 46 Part D: Creating, saving and loading shapes
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PART A: SETTING UP PARAMETERS, SUBROUTINES, AND COLORS

Programmer's Aid #1 provides your APPLE with the ability to do high-resolution color graphics from Integer BASIC. You may plot dots, lines and shapes in a wide variety of detailed forms, in 6 different colors (4 colors on systems below S/N 6000), displayed from two different "pages" of memory. The standard low-resolution graphics allowed you to plot 40 squares across the screen by 47 squares from top to bottom of the screen. This high-resolution graphics display mode lets you plot in much smaller dots, 280 horizontally by 192 vertically. Because 8K bytes of memory (in locations from 8K to 16K, for Page 1) are dedicated solely to maintaining the high-resolution display, your APPLE must contain at least 16K bytes of memory. To use the Page 2 display (in locations from 16K to 24K), a system with at least 24K bytes of memory is needed. If your system is using the Disk Operating System (DOS), that occupies the top 10.5K of memory: you will need a minimum 32K system for Page 1, or 36K for Page 1 and Page 2. See the MEMORY MAP on page 63 for more details.

POSITIONING THE HIGH-RESOLUTION PARAMETERS

The first statement of an Integer BASIC program intending to use the Programmer's Aid High-Resolution subroutines should be:

 \emptyset $X\emptyset = Y\emptyset = COLR = SHAPE = ROT = SCALE$

The purpose of this statement is simply to place the six BASIC variable names used by the High-Resolution feature (with space for their values) into APPLE's "variable table" in specific, known locations. When line \emptyset is executed, the six High-Resolution graphics parameters will be assigned storage space at the very beginning of the variable table, in the exact order specified in line Ø. Your BASIC program then uses those parameter names to change the six parameter values in the variable table. However, the High-Resolution subroutines ignore the parameter names, and look for the parameter values in specific variable-table locations. That is why the program's first line must place the six High-Resolution graphics parameters in known variable-table locations. Different parameter names may be used, provided that they contain the same number of characters. Fixed parameter-name lengths are also necessary to insure that the parameter-value storage locations in the variable table do not change. For example, the name HI could be used in place of $X\emptyset$, but X or XCOORD could not.

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"A2MAN 030-0026-01 3 40.PICT" 427 KB 2001-07-22 dpi: 600h x 600v pix: 2949h x 4240v

The parameters SHAPE, ROT, and SCALE are used only by the subroutines that draw shapes (DRAW and DRAWI, see PART E). These parameters may be omitted from programs using only the PLOT and LINE features:

```
\emptyset X\emptyset = Y\emptyset = COLR
```

Omitting unnecessary parameter definitions speeds up the program during execution. However, you can omit only those unused parameters to the <u>right</u> of the last parameter which <u>is</u> used. Each parameter that is used <u>must</u> be in its proper place, relative to the first parameter in the definition list.

DEFINING SUBROUTINE NAMES

After the six parameters have been defined, the twelve High-Resolution subroutines should be given names, and these names should be assigned corresponding subroutine entry addresses as values. Once defined in this way, the various subroutines can be called by name each time they are used, rather than by numeric address. When subroutines are called by name, the program is easier to type, more likely to be error-free, and easier to follow and to debug.

```
5 INIT = -12288 : CLEAR = -12274 : BKGND = -11471
6 POSN = -11527 : PLOT = -115\emptyset6 : LINE = -115\emptyset\emptyset
7 DRAW = -11465 : DRAW1 = -11462
8 FIND = -1178\emptyset : SHLOAD = -11335
```

Any variable names of any length may be used to call these subroutines. If you want maximum speed, do not define names for subroutines that you will not use in your program.

DEFINING COLOR NAMES

Colors may also be specified by name, if a defining statement is added to the program. Note that GREEN is preceded by LET to avoid a SYNTAX ERROR, due to conflict with the GR command.

```
1Ø BLACK = Ø: LET GREEN = 42: VIOLET = 85
11 WHITE = 127: ORANGE = 17Ø: BLUE = 213
12 BLACK2 = 128: WHITE2 = 255
```

Any integer from \emptyset through 255 may be used to specify a color, but most of the numbers not named above give rather unsatisfactory "colors". On systems below S/N 6000, 170 will appear as green and 213 will appear as violet.

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Once again, unnecessary variable definitions should be omitted, as they will slow some programs. Therefore, a program should not define VIOLET = 85 unless it uses the color VIOLET.

The following example illustrates condensed initialization for a program using only the INIT, PLOT, and DRAW subroutines, and the colors GREEN and WHITE.

```
\emptyset X\emptyset = Y\emptyset = COLR = SHAPE = ROT = SCALE
5 INIT = -12288 : PLOT = -115\emptyset6 : DRAW = -11465
1\emptyset LET GREEN = 42 : WHITE = 127
```

(Body of program would go here)

SPEEDING UP YOUR PROGRAM

Where maximum speed of execution is necessary, any of the following techniques will help:

- 1. Omit the name definitions of colors and subroutines, and refer to colors and subroutines by numeric value, not by name.
- 2. Define the most frequently used program variable names <u>before</u> defining the subroutine and color names (lines 5 through 12 in the previous examples). The example below illustrates how to speed up a program that makes very frequent use of program variables I, J, and K:

```
Ø XØ = YØ = COLR = SHAPE = ROT = SCALE
2 I = J = K
5 INIT = -12288 : CLEAR = -12274
6 BKGND = -11471 : POSN = -11527
1Ø BLACK = Ø : VIOLET = 85
```

3. Use the High-Resolution graphics parameter names as program variables when possible. Because they are defined first, these parameters are the BASIC variables which your program can find fastest.

PART B: PREPARING THE SCREEN FOR GRAPHICS

THE INITIALIZATION SUBROUTINE

In order to use CLEAR, BKGND, POSN, PLOT, or any of the other High-Resolution subroutine CALLs, the INITialization subroutine itself must first be CALLed:

CALL INIT

The INITialization subroutine turns on the high-resolution display and clears the high-resolution screen to black. INIT also sets up certain variables necessary for using the other High-Resolution subroutines. The display consists of a graphics area that is 280 x-positions wide (X0=0 through X0=279) by 160 y-positions high (Y0=0 through Y0=159), with an area for four lines of text at the bottom of the screen. Y0 values from 0 through 191 may be used, but values greater than 159 will not be displayed on the screen. The graphics origin (X0=0, Y0=0) is at the top left corner of the screen.

CHANGING THE GRAPHICS SCREEN

If you wish to devote the entire display to graphics (280 x-positions wide by 192 y-positions high), use

POKE -163Ø2, Ø

The split graphics-plus-text mode may be restored at any time with

POKE -163Ø1, Ø

or another

CALL INIT

When the High-Resolution subroutines are first initialized, all graphics are done in Page 1 of memory (\$2000-3FFF), and only that page of memory is displayed. If you wish to use memory Page 2 (\$4000-5FFF), two POKEs allow you to do so:

POKE 806, 64

causes subsequent graphics instructions to be executed in Page 2, unless those instructions attempt to <u>continue</u> an instruction from Page 1 (for instance, a LINE is always drawn on the same memory page where the last previous point was plotted). After this POKE, the display will still show memory Page 1.

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"A2MAN 030-0026-01 3 43.PICT" 316 KB 2001-07-22 dpi: 600h x 600v pix: 2959h x 4221v

To see what you are plotting on Page 2,

POKE -16299, Ø

will cause Page 2 to be displayed on the screen. You can switch the screen display back to memory Page 1 at any time, with

POKE -163ØØ, Ø

while

POKE 8Ø6, 32

will return you to Page 1 plotting. This last POKE is executed automatically by INIT.

CLEARING THE SCREEN

If at any time during your program you wish to clear the current plotting page to black, use

CALL CLEAR

This immediately erases anything plotted on the <u>current</u> plotting page. INIT first resets the current plotting page to memory Page 1, and then clears Page 1 to black.

The entire current plotting page can be set to any solid background color with the BKGND subroutine. After you have INITialized the High-Resolution subroutines, set COLR to the background color you desire, and then

CALL BKGND

The following program turns the entire display violet:

- \emptyset $X\emptyset = Y\emptyset = COLR : REM SET PARAMETERS$
- 5 INIT = -12288 : BKGND = -11471 : REM DEFINE SUBROUTINES
- 10 VIOLET = 85 : REM DEFINE COLOR
- 20 CALL INIT: REM INITIALIZE HIGH-RESOLUTION SUBROUTINES
- 30 COLR = VIOLET : REM ASSIGN COLOR VALUE
- 40 CALL BKGND: REM MAKE ALL OF DISPLAY VIOLET
- 50 END

PART C: PLOTTING POINTS AND LINES

Points can be plotted anywhere on the high-resolution display, in any valid color, with the use of the PLOT subroutine. The PLOT subroutine can only be used after a CALL INIT has been executed, and after you have assigned appropriate values to the parameters XØ, YØ and COLR. XØ must in the range from Ø through 279, YØ must be in the range from Ø through 191, and COLR must be in the range from Ø through 255, or a

*** RANGE ERR

message will be displayed and the program will halt.

The program below plots a white dot at X-coordinate 35, Y-coordinate 55, and a violet dot at X-coordinate 85, Y-coordinate 90:

```
Ø XØ = YØ = COLR : REM SET PARAMETERS
5 INIT = -12288 : PLOT = -115Ø6 : REM DEFINE SUBROUTINES
1Ø WHITE = 127 : VIOLET = 85 : REM DEFINE COLORS
2Ø CALL INIT : REM INITIALIZE SUBROUTINES
3Ø COLR = WHITE : REM ASSIGN PARAMETER VALUES
4Ø XØ = 35 : YØ = 55
5Ø CALL PLOT : REM PLOT WITH ASSIGNED PARAMETER VALUES
6Ø COLR = VIOLET : REM ASSIGN NEW PARAMETER VALUES
7Ø XØ = 85 : YØ = 9Ø
8Ø CALL PLOT : REM PLOT WITH NEW PARAMETER VALUES
9Ø END
```

The subroutine POSN is exactly like PLOT, except that nothing is placed on the screen. COLR must be specified, however, and a subsequent DRAWl (see PART E) will take its color from the color used by POSN. This subroutine is often used when establishing the origin-point for a LINE.

Connecting any two points with a straight line is done with the LINE subroutine. As with the PLOT subroutine, a CALL INIT must be executed, and XØ, YØ, and COLR must be specified. In addition, before the LINE subroutine can be CALLed, the line's point of origin must have been plotted with a CALL PLOT or as the end point of a previous line or shape. Do not attempt to use CALL LINE without first plotting a point for the line's origin, or the line may be drawn in random memory locations, not necessarily restricted to the current memory page. Once again, XØ and YØ (the coordinates of the termination point for the line), and COLR must be assigned legitimate values, or an error may occur.

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The following program draws a grid of green lines vertically and violet lines horizontally, on a white background:

```
\emptyset X\emptyset = Y\emptyset = COLR : REM SET PARAMETERS, THEN DEFINE SUBROUTINES
5 INIT = -12288: BKGND = -11471: PLOT = -11506: LINE = -11500
10 LET GREEN = 42 : VIOLET = 85 : WHITE = 127 : REM DEFINE COLORS
20 CALL INIT: REM INITIALIZE HIGH-RESOLUTION SUBROUTINES
3Ø POKE -163Ø2, Ø: REM SET FULL-SCREEN GRAPHICS
40 COLR = WHITE : CALL BKGND : REM MAKE THE DISPLAY ALL WHITE
50 COLR = GREEN: REM ASSIGN PARAMETER VALUES
6\emptyset FOR X\emptyset = \emptyset TO 27\emptyset STEP 1\emptyset
7\emptyset Y\emptyset = \emptyset: CALL PLOT: REM PLOT A STARTING-POINT AT TOP OF SCREEN
8Ø YØ = 19Ø: CALL LINE: REM DRAW A VERTICAL LINE TO BOTTOM OF SCREEN
90 NEXT X0: REM MOVE RIGHT AND DO IT AGAIN
100 COLR = VIOLET : REM ASSIGN NEW PARAMETER VALUES
110 FOR Y\emptyset = \emptyset TO 190 STEP 1\emptyset
120 X \emptyset = \emptyset: CALL PLOT: REM PLOT A STARTING-POINT AT LEFT EDGE OF SCREEN
130 \times0 = 270 : CALL LINE : REM PLOT A HORIZONTAL LINE TO RIGHT EDGE
140 NEXT YO: REM MOVE DOWN AND DO IT AGAIN
15Ø END
```

PART D: CREATING, SAVING AND LOADING SHAPES

INTRODUCTION

The High-Resolution feature's subroutines provide the ability to do a wide range of high-resolution graphics "shape" drawing. A "shape" is considered to be any figure or drawing (such as an outline of a rocket ship) that the user wishes to draw on the display many times, perhaps in different sizes, locations and orientations. Up to 255 different shapes may be created, used, and saved in a "Shape Table", through the use of the High-Resolution subroutines DRAW, DRAWl and SHLOAD, in conjunction with parameters SHAPE, ROT and SCALE.

In this section, PART D, you will be shown how to create, save and load a Shape Table. The following section, PART E, demonstrates the use of the shape-drawing subroutines with a predefined Shape Table.

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"A2MAN 030-0026-01 3 46.PICT" 326 KB 2001-07-22 dpi: 600h x 600v pix: 2886h x 4194v

HOW TO CREATE A SHAPE TABLE

Before the High-Resolution shape-drawing subroutines can be used, a shape must be defined by a "shape definition." This shape definition consists of a sequence of plotting vectors that are stored in a series of bytes in APPLE's memory. One or more such shape definitions, with their index, make up a "Shape Table" that can be created from the keyboard and saved on disk or cassette tape for future use.

Each byte in a shape definition is divided into three sections, and each section can specify a "plotting vector": whether or not to plot a point, and also a direction to move (up, down, left, or right). The shape-drawing subroutines DRAW and DRAWl (see PART E) step through each byte in the shape definition section by section, from the definition's first byte through its last byte. When a byte that contains all zeros is reached, the shape definition is complete.

This is how the three sections A, B and C are arranged within one of the bytes that make up a shape definition:

Section:	~	_	_	В	_	_	A		
Bit Number:	7	6	5	4	3	2	1	Ø	
Specifies:	D	D	P	D	D	P	D	D	

Each bit pair DD specifies a direction to move, and each bit P specifies whether or not to plot a point before moving, as follows:

Notice that the last section, C (the two most significant bits), does not have a P field (by default, $P=\emptyset$), so section C can only specify a move without plotting.

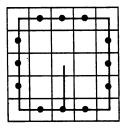
Each byte can represent up to three plotting vectors, one in section A, one in section B, and a third (a move only) in section C.

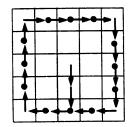
DRAW and DRAWl process the sections from right to left (least significant bit to most significant bit: section A, then B, then C). At any section in the byte, IF ALL THE REMAINING SECTIONS OF THE BYTE CONTAIN ONLY ZEROS, THEN THOSE SECTIONS ARE IGNORED. Thus, the byte cannot end with a move in section C of ØØ (a move up, without plotting) because that section, containing only zeros, will be ignored. Similarly, if section C is ØØ (ignored), then section B cannot be a move of ØØØ as that will also be ignored. And a move of ØØØ in section A will end your shape definition unless there is a 1-bit somewhere in section B or C.

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Suppose you want to draw a shape like this:

First, draw it on graph paper, one dot per square. Then decide where to start drawing the shape. Let's start this one at the center. Next, draw a path through each point in the shape, using only 90 degree angles on the turns:



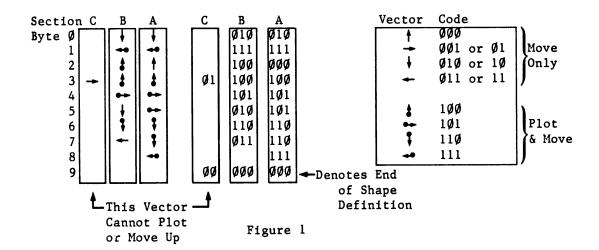


Next, re-draw the shape as a series of plotting vectors, each one moving one place up, down, right, or left, and distinguish the vectors that plot a point before moving (a dot marks vectors that plot points).

Now "unwrap" those vectors and write them in a straight line:



Next draw a table like the one in Figure 1, below:



For each vector in the line, determine the bit code and place it in the next available section in the table. If the code will not fit (for example, the vector in section C can't plot a point), or is a ØØ (or ØØØ) at the end of a byte, then skip that section and go on to the next. When you have finished coding all your vectors, check your work to make sure it is accurate.

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"A2MAN 030-0026-01 3 48.PICT" 325 KB 2001-07-22 dpi: 600h x 600v pix: 2931h x 4392v

Now make another table, as shown in Figure 2, below, and re-copy the vector codes from the first table. Recode the vector information into a series of hexadecimal bytes, using the hexadecimal codes from Figure 3.

Section	ı: C F	A	Bytes Recoded	Codes
		<u> </u>	in Hex	Rinary Hay
D A	a a a ı	0 0 1 0		Binary Hex
Byte Ø	Ø Ø Ø 1	Ø Ø 1 Ø	= 1 2	$\emptyset\emptyset\emptyset\emptyset = \emptyset$
1	Ø Ø 1 1	1 1 1 1	= 3 F	$\emptyset\emptyset\emptyset1 = 1$
2	Ø Ø 1 Ø	0000	= 2 Ø	$\emptyset\emptyset1\emptyset = 2$
3	Ø 1 1 Ø	0100	= 64	$\emptyset \emptyset 11 = 3$
4	Ø Ø 1 Ø	1101	= 2 D	$\emptyset 1 \emptyset \emptyset = 4$
5	Ø Ø Ø 1	Ø 1 Ø 1	= 15	0101 = 5
6	Ø Ø 1 1	Ø 1 1 Ø	= 36	$\emptyset 11\emptyset = 6$
7	Ø Ø Ø 1	1 1 1 Ø	= 1 E	0111 = 7
8	Ø Ø Ø Ø	Ø 1 1 1	= Ø 7	1000 = 8
9	Ø Ø Ø Ø	Ø Ø Ø Ø	= Ø Ø ← Denotes End	1001 = 9
			of Shape	1010 = A
Hex:	Digit l	Digit 2	Definition	1011 = B
				1100 = C
				$11\emptyset 1 = D$
				1110 = E
		Figure 2		1111 = F

The series of hexadecimal bytes that you arrived at in Figure 2 is the shape definition. There is still a little more information you need to provide before you have a complete Shape Table. The form of the Shape Table, complete with its <u>index</u>, is shown in Figure 4 on the next page.

For this example, your index is easy: there is only one shape definition. The Shape Table's starting location, whose address we have called S, must contain the number of shape definitions (between Ø and 255) in hexadecimal. In this case, that number is just one. We will place our shape definition immediately below the index, for simplicity. That means, in this case, the shape definition will start in byte S+4: the address of shape definition #1, relative to S, is 4 (ØØ Ø4, in hexadecimal). Therefore, index byte S+2 must contain the value Ø4 and index byte S+3 must contain the value Ø0. The completed Shape Table for this example is shown in Figure 5 on the next page.

Figure 3

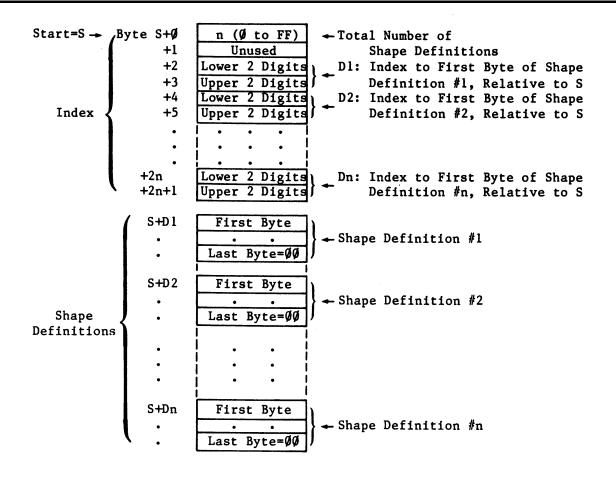


Figure 4

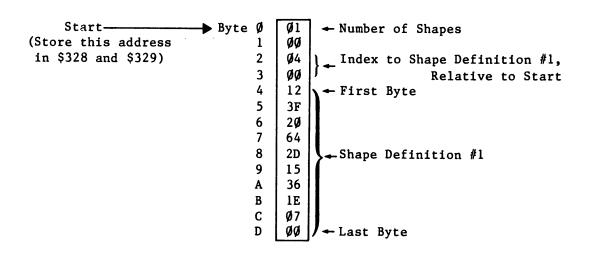


Figure 5

50

"A2MAN 030-0026-01 3 50.PICT" 204 KB 2001-07-22 dpi: 600h x 600v pix: 2760h x 4257v

You are now ready to type the Shape Table into APPLE's memory. First, choose a starting address. For this example, we'll use hexadecimal address \$800.

Note: this address <u>must</u> be less than the highest memory address available in your system (HIMEM), and not in an area that will be cleared when you use memory Page 1 (hexadecimal locations \$2000 to \$4000) or Page 2 (hexadecimal locations \$4000 to \$6000) for high-resolution graphics. Furthermore, it must not be in an area of memory used by your BASIC program. Hexadecimal 0800 (2048, in decimal) is the lowest memory address normally available to a BASIC program. This lowest address is called LOMEM. Later on, we will move the LOMEM pointer higher, to the end of our Shape Table, in order to protect our table from BASIC program variables.

Press the RESET key to enter the Monitor program, and type the Starting address for your Shape Table:

Ø8ØØ

If you press the RETURN key now, APPLE will show you the address and the contents of that address. That is how you examine an address to see if you have a put the correct number there. If instead you type a colon (:) followed by a two-digit hexadecimal number, that number will be stored at the specified address when you press the RETURN key. Try this:

Ø8ØØ return

(type "return" by pressing the RETURN key). What does APPLE say the contents of location $\emptyset 8 \emptyset \emptyset$ are? Now try this:

Ø8ØØ:Øl return Ø8ØØ return Ø8ØØ- Øl

The APPLE now says that the value \emptyset l (hexadecimal) is stored in the location whose address is \emptyset 8 \emptyset 0. To store more two-digit hexadecimal numbers in successive bytes in memory, just open the first address:

Ø8ØØ:

and then type the numbers, separated by spaces:

Ø8ØØ:Ø1 ØØ Ø4 ØØ 12 3F 2Ø 64 2D 15 36 1E Ø7 ØØ return

You have just typed your first complete Shape Table...not so bad, was it? To check the information in your Shape Table, you can examine each byte separately or simply press the RETURN key repeatedly until all the bytes of interest (and a few extra, probably) have been displayed:

Ø8ØØ return
Ø8ØØ- Ø1
return
ØØ Ø4 ØØ 12 3F 2Ø 64
return
Ø8Ø8- 2D 15 36 1E Ø7 ØØ FF FF

If your Shape Table looks correct, all that remains is to store the starting address of the Shape Table where the shape-drawing subroutines can find it (this is done automatically when you use the SHLOAD subroutine to get a table from cassette tape). Your APPLE looks for the four hexadecimal digits of the table's starting address in hexadecimal locations 328 (lower two digits) and 329 (upper two digits). For our table's starting address of \$\psi\$8 \$\psi\$0, this would do the trick:

328:00 08

To protect this Shape Table from being erased by the variables in your BASIC program, you must also set LOMEM (the lowest memory address available to your program) to the address that is one byte beyond the Shape Table's last, or largest, address.

It is best to set LOMEM from BASIC, as an immediate-execution command issued before the BASIC program is RUN. LOMEM is automatically set when you invoke BASIC (reset ctrl B return) to decimal 2048 (0800, in hexadecimal). You must then change LOMEM to 2048 plus the number of bytes in your Shape Table plus one. Our Shape Table was decimal 14 bytes long, so our immediate-execution BASIC command would be:

LOMEM: 2048 + 15

Fortunately, all of this (entering the Shape Table at LOMEM, resetting LOMEM to protect the table, and putting the table's starting address in \$328-\$329) is taken care of automatically when you use the High-Resolution feature's SHLOAD subroutine to get the table from cassette tape.

SAVING A SHAPE TABLE

Saving on Cassette Tape

To save your Shape Table on tape, you must be in the Monitor and you must know three hexadecimal numbers:

- 1) Starting Address of the table (0800, in our example)
- 2) Last Address of the table (080D, in our example)
- 3) Difference between 2) and 1) (000D, in our example)

Item 3, the difference between the last address and the first address of the table, must be stored in hexadecimal locations \emptyset (lower two digits) and l (upper two digits):

Ø:ØD ØØ return

Now you can "Write" (store on cassette) first the table length that is stored in locations \emptyset and 1, and then the Shape Table itself that is stored in locations Starting Address through Last Address:

Ø.1W Ø8ØØ.Ø8ØDW

Don't press the RETURN key until you have put a cassette in your tape recorder, rewound it, and started it recording (press PLAY and RECORD simultaneously). Now press the computer's RETURN key.

Saving on Disk

To save your Shape Table on disk, use a command of this form:

BSAVE filename, A\$ startingaddress, L\$ tablelength

For our example, you might type

BSAVE MYSHAPE1, A\$ Ø8ØØ, L\$ ØØØD

Note: the Disk Operating System (DOS) occupies the top 10.5K of memory (10752 bytes decimal, or \$2A00 hex); make sure your Shape Table is not in that portion of memory when you "boot" the disk system.

LOADING A SHAPE TABLE

Loading from Cassette Tape

To load a Shape Table from cassette tape, rewind the tape, start it playing (press PLAY), and (in BASIC, now) type

CALL -11335 return

or (if you have previously assigned the value -11335 to the variable SHLOAD)

CALL SHLOAD return

You should hear one "beep" when the table's length has been read successfully, and another "beep" when the table itself has been read. When loaded this way, your Shape Table will load into memory, beginning at hexadecimal address \$8\$\$. LOMEM is automatically changed to the address of the location immediately following the last Shape-Table byte. Hexadecimal locations 328 and 329 are automatically set to contain the starting address of the Shape Table.

Loading from Disk

To load a Shape Table from disk, use a command of the form

BLOAD filename

From our previously-saved example, you would type

BLOAD MYSHAPE1

This will load your Shape Table into memory, beginning at the address you specified after "A\$" when you BSAVEd the Shape Table earlier. In our example, MYSHAPEl would BLOAD beginning at address Ø8ØØ. You must store the Shape Table's starting address in hexadecimal locations 328 and 329, yourself, from the Monitor:

328:00 08 return

If your Shape Table is in an area of memory that may be used by your BASIC program (as our example is), you must protect the Shape Table from your program. Our example lies at the low end of memory, so we can protect it by raising LOMEM to just above the last byte of the Shape Table. This must be done after invoking BASIC (reset ctrl B return) and before RUNning our BASIC program. We could do this with the immediate-execution BASIC command

LOMEM: 20/48 + 15

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"A2MAN 030-0026-01 3 54.PICT" 278 KB 2001-07-22 dpi: 600h x 600v pix: 2796h x 4077v

FIRST USE OF A SHAPE TABLE

You are now ready to write a BASIC program using Shape-Table subroutines such as DRAW and DRAWl. For a full discussion of these High-Resolution subroutines, see the following section, PART E.

Remember that Page 1 graphics uses memory locations 8192 through 16383 (8K to 16K), and Page 2 graphics uses memory locations 16384 through 24575 (16K to 24K). Integer BASIC puts your program right at the top of available memory; so if your APPLE contains less than 32K of memory, you should protect your program by setting HIMEM to 8192. This must be done after you invoke BASIC (reset ctrl B return) and before RUNning your program, with the immediate-execution command

HIMEM: 8192

Here's a sample program that assumes our Shape Table has already been loaded from tape, using CALL SHLOAD. This program will print our defined shape, rotate it 5.6 degrees if that rotation is recognized (see ROT discussion, next section) and then repeat, each repetition larger than the one before.

```
10 XØ = YØ = COLR = SHAPE = ROT = SCALE : REM SET PARAMETERS
20 INIT = -12288 : DRAW = -11465 : REM DEFINE SUBROUTINES
30 WHITE = 127 : BLACK = Ø : REM DEFINE COLORS
40 CALL INIT : REM INITIALIZE HIGH-RESOLUTION SUBROUTINES
50 SHAPE = 1
60 XØ = 139 : YØ = 79 : REM ASSIGN PARAMETER VALUES
70 FOR R = 1 TO 48
80 ROT = R
90 SCALE = R
100 COLR = WHITE
110 CALL DRAW : REM DRAW SHAPE 1 WITH ABOVE PARAMETERS
120 NEXT R : REM NEW PARAMETERS
130 END
```

To pause, and then erase each square after it is drawn, add these lines:

```
114 FOR PAUSE = 1 TO 200 : NEXT PAUSE
116 COLR = BLACK : REM CHANGE COLOR
118 CALL DRAW : REM RE-DRAW SAME SHAPE, IN NEW COLOR
```

PART E: DRAWING SHAPES FROM A PREPARED SHAPE TABLE

Before either of the two shape-drawing subroutines DRAW or DRAWl can be used, a "Shape Table" must be defined and stored in memory (see PART E: CREATING A SHAPE TABLE), the Shape Table's starting address must be specified in hexadecimal locations 328 and 329 (808 and 809, in decimal), and the High-Resolution subroutines themselves must have been initialized by a CALL INIT.

ASSIGNING PARAMETER VALUES

The DRAW subroutine is used to display any of the shapes defined in the current Shape Table. The origin or 'beginning point' for DRAWing the shape is specified by the values assigned to XØ and YØ, and the rest of the shape continues from that point. The color of the shape to be DRAWn is specified by the value of COLR.

The shape number (the Shape Table's particular shape definition that you wish to have DRAWn) is specified by the value of SHAPE. For example,

SHAPE = 3

specifies that the next shape-drawing command will use the third shape definition in the Shape Table. SHAPE may be assigned any value (from 1 through 255) that corresponds to one of the shape definitions in the current Shape Table. An attempt to DRAW a shape that does not exist (by executing a shape-drawing command after setting SHAPE = 4, when there are only two shape definitions in your Shape Table, for instance) will result in a *** RANGE ERR message being displayed, and the program will halt.

The relative size of the shape to be DRAWn is specified by the value assigned to SCALE. For example,

SCALE = 4

specifies that the next shape DRAWn will be four times the size that is described by the appropriate shape definition. That is, each "plotting vector" (either a plot and a move, or just a move) will be repeated four times. SCALE may be assigned any value from Ø through 255, but SCALE = Ø is interpreted as SCALE = 256, the largest size for a given shape definition.

You can also specify the orientation or angle of the shape to be DRAWn, by assigning the proper value to ROT. For example,

 $ROT = \emptyset$

will cause the next shape to be DRAWn oriented just as it was defined, while

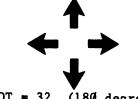
ROT = 16

will cause the next shape to be DRAWn rotated 90 degrees clockwise. The value assigned to ROT must be within the range 0 to 255 (although ROT=64, specifying a rotation of 360 degrees clockwise, is the equivalent of ROT=0). For SCALE=1, only four of the 63 different rotations are recognized (0,16,32,48); for SCALE=2, eight different rotations are recognized; etc. ROT values specifying unrecognized rotations will usually cause the shape to be DRAWn with the next smaller recognized rotation.

ORIENTATIONS OF SHAPE DEFINITION

ROT = Ø (no rotation from shape definition)

ROT = 48 (270 degrees
 clockwise rotation)



ROT = 16 (90 degrees
 clockwise rotation)

ROT = 32 (180 degrees clockwise rotation)

DRAWING SHAPES

The following example program DRAWs shape definition number three, in white, at a 135 degree clockwise rotation. Its starting point, or origin, is at (140,80).

- Ø XØ = YØ = COLR = SHAPE = ROT = SCALE : REM SET PARAMETERS
- 5 INIT = -12288 : DRAW = -11465 : REM DEFINE SUBROUTINES
- 10 WHITE = 127 : REM DEFINE COLOR
- 20 CALL INIT: REM INITIALIZE HIGH-RESOLUTION SUBROUTINES
- $3\emptyset$ $X\emptyset = 14\emptyset$: $Y\emptyset = 8\emptyset$: COLR = WHITE : REM ASSIGN PARAMETER VALUES
- 40 SHAPE = 3 : ROT = 24 : SCALE = 2
- 50 CALL DRAW: REM DRAW SHAPE 3, DOUBLE SIZE, TURNED 135 DEGREES
- 60 END

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"A2MAN 030-0026-01 3 57.PICT" 261 KB 2001-07-22 dpi: 600h x 600v pix: 2787h x 4086v

LINKING SHAPES

DRAWl is identical to DRAW, except that the last point previously DRAWn, PLOTted or POSNed determines the color and the starting point for the new shape. XØ, YØ, and COLR, need not be specified, as they will have no effect on DRAWl. However, some point must have been plotted before CALLing DRAWl, or this CALL will have no effect.

The following example program draws "squiggles" by DRAWing a small shape whose orientation is given by game control #0, then linking a new shape to the old one, each time the game control gives a new orientation. To clear the screen of "squiggles," press the game-control button.

```
1Ø XØ = YØ = COLR = SHAPE = ROT = SCALE : REM SET PARAMETERS
2Ø INIT = -12288 : DRAW = -11465 : DRAW1 = -11462
22 CLEAR = -12274 : WHITE = 127 : REM NAME SUBROUTINES AND COLOR
3Ø FULLSCREEN = -163Ø2 : BUTN = -16287 : REM NAME LOCATIONS
4Ø CALL INIT : REM INITIALIZE HIGH-RESOLUTION SUBROUTINES
5Ø POKE FULLSCREEN, Ø : REM SET FULL-SCREEN GRAPHICS
6Ø COLR = WHITE : SHAPE = 1 : SCALE = 5
7Ø XØ = 14Ø : YØ = 8Ø : REM ASSIGN PARAMETER VALUES
8Ø CALL CLEAR : ROT = PDL(Ø) : CALL DRAW : REM DRAW FIRST SHAPE
9Ø IF PEEK(BUTN) > 127 THEN GOTO 8Ø : REM PRESS BUTTON TO CLEAR SCREEN
1ØØ R = PDL(Ø) : IF (R < ROT+2) AND (R > ROT-2) THEN GOTO 9Ø :
REM WAIT FOR CHANGE IN GAME CONTROL
11Ø ROT = R : CALL DRAW1 : REM ADD TO "SQUIGGLE"
12Ø GOTO 9Ø : REM LOOK FOR ANOTHER CHANGE
```

After DRAWing a shape, you may wish to draw a LINE from the last plotted point of the shape to another fixed point on the screen. To do this, once the shape is DRAWn, you must first use

CALL FIND

prior to CALLing LINE. The FIND subroutine determines the X and Y coordinates of the final point in the shape that was DRAWn, and uses it as the beginning point for the subsequent CALL LINE.

The following example DRAWs a white shape, and then draws a violet LINE from the final plot position of the shape to the point (10, 25).

COLLISIONS

Any time two or more shapes intersect or overlap, the new shape has points in common with the previous shapes. These common points are called points of "collision."

The DRAW and DRAW1 subroutines return a "collision count" in the hexadecimal memory location \$32A (810, in decimal). The collision count will be constant for a fixed shape, rotation, scale, and background, provided that no collisions with other shapes are detected. The difference between the "standard" collision value and the value encountered while DRAWing a shape is a true collision counter. For example, the collision counter is useful for determining whether or not two constantly moving shapes ever touch each other.

```
110 CALL DRAW: REM DRAW THE SHAPE
120 COUNT = PEEK(810): REM FIND THE COLLISION COUNT
```

PART F: TECHNICAL INFORMATION

LOCATIONS OF THE HIGH-RESOLUTION PARAMETERS

When the high-resolution parameters are entered (line Ø, say), they are stored -- with space for their values -- in the BASIC variable table, just above LOMEM (the LOWest MEMory location used for BASIC variable storage). These parameters appear in the variable table in the exact order of their first mention in the BASIC program. That order must be as shown below, because the High-Resolution subroutines look for the parameter values by location only. Each parameter value is two bytes in length. The low-order byte is stored in the lesser of the two locations assigned.

VARIABLE-TABLE PARAMETER LOCATIONS

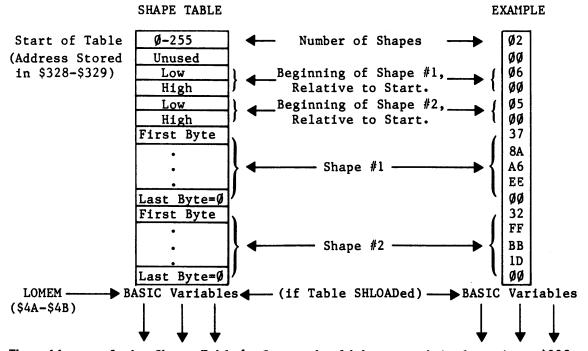
Parameter	Locations	beyond LOMEM
ΧØ	\$Ø5,	\$Ø6
YØ	\$ Ø C,	\$ØD
COLR	\$15,	\$16
SHAPE	\$1F,	\$20
ROT	\$27,	\$28
SCALE	\$31,	\$32

VARIABLES USED WITHIN THE HIGH-RESOLUTION SUBROUTINES

Variable <u>Name</u>	Hexadecimal Location	Description
SHAPEL, SHAPEH	1A, 1B	On-the-fly shape pointer.
HCOLOR 1	1C	On-the-fly color byte.
COUNTH	1D	High-order byte of step count for LINE.
HBASL, HBASH	26, 27	On-the-fly BASE ADDRESS
HMASK	3Ø	On-the-fly BIT MASK
QDRNT	53	2 LSB's are rotation quadrant for DRAW.
XOL, XOH	320, 321	Most recent X-coordinate. Used for initial endpoint of LINE. Updated by PLOT, POSN, LINE and FIND, not DRAW.
YO	322	Most recent Y-coordinate (see XOL, XOH).
BXSAV	323	Saves 6502 X-register during high- resolution CALLs from BASIC.
HCOLOR	324	Color specification for PLOT, POSN.
HNDX	325	On-the-fly byte index from BASE ADDRESS.
HPAG	326	Memory page for plotting graphics. Normally \$20 for plotting in Page 1 of high-resolution display memory (\$2000-\$3FFF).
SCALE	327	On-the-fly scale factor for DRAW.
SHAPXL, SHAPXH	328, 329	Start of Shape Table pointer.
COLLSN	32A	Collision count from DRAW, DRAW1.

SHAPE TABLE INFORMATION

Shape Tape	Description
Record #1	A two-byte-long record that contains the length of record #2, Low-order first.
Record Gap Record #2	Minumum of .7 seconds in length. The Shape Table (see below).

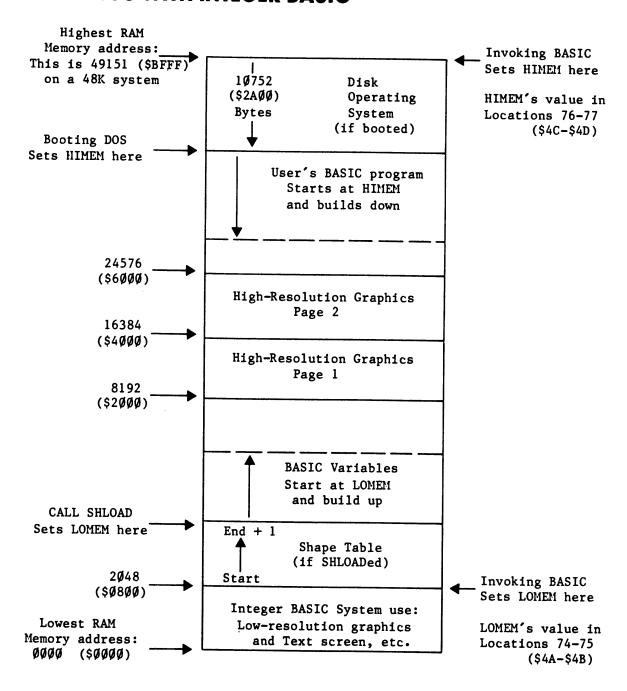


The address of the Shape Table's Start should be stored in locations \$328 and \$329. If the SHLOAD subroutine is used to load the table, Start will be set to LOMEM (normally this is at \$0800) and then LOMEM will be moved to one byte after the end of the Shape Table, automatically.

If you wish to load a Shape Table named MYSHAPES2 from disk, beginning at decimal location 2048 (0800 hex), and ending at decimal location 2048 plus decimal 15 bytes (as in the example above), you may wish to begin your BASIC program as follows:

- Ø D\$ = "" : REM QUOTES CONTAIN CTRL D (D\$ WILL BE ERASED BY SHAPE TABLE)
- 1 PRINT D\$; "BLOAD MYSHAPES2, A 2048": REM LOADS SHAPE TABLE
- 2 POKE 808, 2048 MOD 256: POKE 809, 2048 / 256: REM SETS TABLE START
- 3 POKE 74, (20/48 + 15 + 1) MOD 256: POKE 75, (20/48 + 15 + 1) / 256
- 4 POKE 204, PEEK(74): POKE 205, PEEK(75): REM SETS LOMEM TO TABLE END+1
- 5 $X\emptyset = Y\emptyset = COLR = SHAPE = ROT = SCALE : REM SETS PARAMETERS$

APPLE II MEMORY MAP FOR USING HIGH-RESOLUTION GRAPHICS WITH INTEGER BASIC



Unfortunately, there is no convention for mapping memory. This map shows the highest (largest) address at the top, lowest (smallest) address at the bottom. The maps of Shape Tables that appear on other pages show the Starting address (lowest and smallest) at the top, the Ending address (highest and largest) at the bottom.

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"A2MAN 030-0026-01 3 63.PICT" 257 KB 2001-07-22 dpi: 600h x 600v pix: 2787h x 4104v

PART G: COMMENTS

1. Using memory Page 1 for high-resolution graphics erases everything in memory from location 8192 (\$2000 hex) to location 16383 (\$3FFF). If the top of your system's memory is in this range (as it will be, if you have a 16K system), Integer BASIC will normally put your BASIC program exactly where it will be erased by INIT. You must protect your program by setting HIMEM below memory Page 1, after invoking BASIC (reset ctrl B return) and before RUNning your program: use this immediate-execution command:

HIMEM: 8192 return

- 2. Using memory Page 2 for high-resolution graphics erases memory from location 16384 (\$4000) to location 24575 (\$5FFF). If yours is a 24K system, this will erase your BASIC program unless you do one of the following:
 - a) never use Page 2 for graphics; or
 - b) change HIMEM to 8192, as described above.
- 3. The picture is further confused if you are also using an APPLE disk with your system. The Disk Operating System (DOS), when booted, occupies the highest 10.5K (\$2A00) bytes of memory. HIMEM is moved to just below the DOS. Therefore, if your system contains less than 32K of memory, the DOS will occupy memory Page 1 and Page 2. In that case, you cannot use the High-Resolution graphics with the DOS intact. An attempt to do so will erase all or part of the DOS. A 32K system can use only Page 1 for graphics without destroying the DOS, but HIMEM must be moved to location 8192 as described above. 48K systems can usually use the DOS and both high-resolution memory pages without problems.
- 4. If you loaded your Shape Table starting at LOMEM in location 2048 (\$0800), from disk or from tape without using SHLOAD, Integer BASIC will erase the Shape Table when it stores the program variables. To protect your Shape Table, you must move LOMEM to one byte beyond the last byte of the Shape Table, after invoking BASIC and before using any variables. SHLOAD does this automatically, but you can use this immediate-execution command:

LOMEM: 20/48 + tablelength + 1

where tablelength must be a number, <u>not</u> a variable name. Some programmers load their Shape Tables beginning in location 3048 (\$0BE8). That leaves a safe margin of 1000 bytes for variables below the Shape Table, and at least 5000 bytes (if HIMEM:8192) above the table for their BASIC program.

5. CALLing an undefined or accidentally misspelled variable name is usually a CALL to location zero (the default value of any undefined variable). This CALL may cause unpredictable and unwelcome results, depending on the contents of location zero. However, after you execute this BASIC command:

POKE Ø, 96

an accidental CALL to location zero will cause a simple jump back to your BASIC program, with no damage.

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APPENDIX SOURCE ASSEMBLY LISTINGS

66	High-Resolution Graphics	\$D ØØØ- \$D3FF
76	Renumber	\$D4 ØØ- \$D4BB
79	Append	\$D4BC+\$D4D4
8Ø	Relocate	\$D4DC-\$D52D
82	Tape Verify (EASIC)	\$D535 - \$D553
83	Tape Verify (65∯2 Code & Data)	\$D 5 54 - \$D 5AA
84	RAM Test	\$D5BC-\$D691
87	Music	\$1) 71 7 _ \$1) 7F3.

66

##20 INIT FOR #2000-3FFF

HPAG HI-RES SCREEN MEMORY.

D000 A9 20

DC02 BD 26 03

63 * 64

65

ORG

OBJ

STA

66 SETHRL LDA

\$D000

\$A000

Apple 2 Ted	chnical Manual	Programmer's Aid # 1
DO05 AD 57 CO DO08 AD 53 CO DO08 AD 50 CO DO06 AP 00 DO10 85 1C DO12 AD 26 03 DO15 85 18 DO17 AO 00 DO19 84 1A DO18 A5 1C DO1D 91 1A DO1F 20 A2 DO DO22 C8 DO23 DO F6 DO25 E6 18 DO27 A5 18 DO29 29 1F DO28 DO EE DO2D 60	68 LDA 69 LDA 70 LDA 71 HCLR LDA 72 BKGNDD STA 73 BKGND LDA 74 STA 75 LDY 76 STY 77 BKGND1 LDA 78 STA 79 JSR 80 INY 81 BNF 82 INC 83 LDA 84 AND 85 BNE 86 RTS	HIRES SET HIRES DISPLAY MODE MIXSET WITH TEXT AT BOTTOM. TXTCLR SET GRAPHICS DISPLAY MODE #\$0 HCOLOR1 SET FOR BLACK BKGNO. HPAG SHAPEH INIT HI-RES SCREEN MEM #\$0 FOR CURRENT PAGE, NORMALLY SHAPEL \$2000-3FFF OR \$4000-5FFF HCOLOR1 (SHAPEL), Y CSHFT2 (SHAPEL, H) WILL SPECIFY 32 SEPARATE PAGES. BKGND1 THROUGHOUT THE INIT. SHAPEH SHAPEH \$\$1F TEST FOR DONE. BKGND1
DO2E 8D 22 03 D031 8E 20 03 D034 8C 21 03 D037 48 D038 29 C0 D038 85 26 D03C 4A D03D 4A D03E 05 26 D042 68 D043 85 27 D045 0A D046 0A D047 0A D048 26 27 D040 0A D048 26 27 D040 0A D048 26 27 D050 A5 20 D070 A5 1C D077 B0 29 D070 A5 1C D077 B1 26	88 * HI-RES G 89 HPOSN STA 90 STX 91 STY 92 PHA 93 AND 94 STA 95 LSR 96 LSR 97 ORA 98 STA 99 PLA 100 STA 101 ASL 102 ASL 103 ASL 104 ROL 105 ASL 106 ROL 107 ASL 108 ROR 109 LDA 110 AND 111 ORA 112 STA 114 CPY 115 BEG 116 LDY 117 ADC 118 HPOSN2 STA 114 CPY 115 BEG 116 LDY 117 ADC 118 HPOSN2 STA 114 CPY 115 BEG 116 LDY 117 ADC 118 HPOSN2 STA 114 CPY 115 BEG 116 LDY 117 ADC 118 HPOSN2 STA 114 CPY 115 BEG 116 LDY 117 ADC 118 HPOSN2 STA 114 CPY 115 BEG 116 LDY 117 ADC 118 HPOSN2 STA 114 STA 115 SBC 120 BCS 121 STY 122 TAX 123 LDA 124 STA 125 TYA 126 LSR 127 LDA 128 HPOSN3 STA 129 BCS 131 HPLOT JSR 132 HPLOT LDA 133	RAPHICS POSITION AND PLOT SUBRS YO ENTER WITH Y IN A-REG, XOL XL IN X-REG, XOH AND XH IN Y-REG. #\$CO HBASL FOR Y-COORD = OOABCDEF. ; CALCULATES BASE ADDRESS ; IN HBASL, HBASH FOR HBASL ACCESSING SCREEN MEM HBASL VIA (HBASL), Y ADDRESSING MODE HBASH ; CALCULATES ; HRASH = PPPFGHCD, ; HRASL = EABABOOO HBASH ; WHERE PPP=001 FOR \$2000-3FFF HBASH SCREEN MEM RANGE AND ; PPP=010 FOR \$4000-7FFF HBASL (GIVEN Y-COORD=ABCDEFGH) HBASH #\$1F HPAQ HBASH DIVIDE XO BY 7 FOR #\$0 INDEX FROM BASE ADR HPOSN2 (GUOTIENT) AND BIT #\$23 WITHIN SCREEN MEM BYTE #\$4 (MASK SPEC'D BY REMAINDER) #\$7 SUBTRACT OUT SEVENS. HPOSN1 INNX WORKS FOR XO FROM O TO 279, LOW-ORDER MSKTBL-249, X BYTE IN X-REG, IMASK HIGH IN Y-REG ON ENTRY ; IF ON ODD BYIE (CARRY SET) HCOLOR THEN ROTATE HCOLOR ONE HCOLOR1 BIT FOR 180 DEGREE SHIFT CSMFT2 PRIOR TO COPYING TO HCOLOR1. HPOSN HCOLOR1 CALC BIT POSN IN HBASL, H (HBASL), Y HNDX, AND HHASK FROM
D033 51 26 D035 91 26 D037 60	134 ANU 135 EOR 136 STA 137 RTS 138 #	HMASK Y-COURD IN A-REG, (HBASL), Y X-COURD IN X,Y-REGS. (HBASL), Y FOR ANY 'L' BITS OF HMASK SUBSTITUTE CORRESPONDING BIT OF HCOLOR1.

```
140 * HI-RES GRAPHICS L.R.U.D SUBRS
D038 10 24
                 141 LFTRT
                            BPL
                                    RIGHT USE SIGN FOR LFT/RT SELECT
DO3A A5 30
                 142 LEFT
                             LDA
                                    HMASK
D03C 4A
                 143
                             LSR
                                    ; SHIFT LOW-ORDER
DOSD BO 05
                 144
                             BCS
                                    LEFT1 7 BITS OF HMASK
DO3F 49 CO
                 145
                             EOR
                                    ##CO ONE BIT TO LSB.
D071 85 30
                 146 LR1
                             STA
                                    HMASK
D073 60
                 147
                             RTS
D074 88
                 148 LEFT1
                             DEY
                                    DECR HORIZ INDEX.
D075 10 02
                 149
                             BPL
                                    LEFT2
D077 A0 27
                 150
                             LDY
                                    #$27 WRAP AROUND SCREEN.
D079 A9 CO
                 151 LEFT2 LDA
                                    ##CO NEW HMAEK, RIGHTMOST
D078 85 30
                 152 NEWNDX STA
                                    HMASK DOT OF BYTE.
D07D 8C 25 03
                 153
                             STY
                                    HNDX UPDATE HORIZ INDEX.
                 154 CSHIFT LDA
DOAO A5 1C
                                    HCOLOR 1
DOM2 OA
                 155 CSHFT2 ASL
                                    ; ROTATE LOW-ORDER
DOAS C9 CO
                 156
                             CMP
                                    #$CO 7 BITS OF HCOLOR1
DOA5 10 06
                 157
                             BPL
                                    RTS1 ONE BIT POSN.
DOA7 A5 1C
                 158
                             LDA
                                    HCOLOR 1
                                    #$7F ZXYXYXYX -> ZYXYXYXY
DOA9 49 7F
                 159
                             EOR
DOAB 85 1C
                             STA
                 160
                                    HCOLOR 1
DOAD 60
                 161 RTS1
                             RTS
DOME A5 30
                 162 RIGHT
                             LDA
                                   HMASK
DOBO OA
                 163
                             ASL
                                    : SHIFT LOW-ORDER
DOB1 49 80
                 164
                             EOR
                                    #$80 7 BITS OF HMASK
DOB3 30 DC
                 165
                             EMI
                                   LR1 ONE BIT TO MSB.
DOB5 A9 81
DOB7 C8
                 166
                             LDA
                                    ##81
                 167
                             INY
                                   NEXT BYTE.
DOBS CO 28
                             CPY
                 168
                                    #$28
DOBA 90 DF
                 169
                             BCC
                                    NEWNDX
DOBC AO OO
                 170
                             LDY
                                    ##0 WRAP AROUND SCREEN IF >279
DOBE BO DB
                 171
                             BCS
                                   NEWNDX ALWAYS TAKEN.
                 173 * L.R.U.D.
                                  SUBROUTINES
DOCO 18
                 174 LRUDX1 CLC
                                    NO 90 DEG ROT (X-OR).
DOC1 A5 51
                 175 LRUDX2 LDA
                                    SHAPEX
DOC3 29 04
                 176
                             AND
                                    #$4 IF B2=0 THEN NO PLOT.
DOC5 FO 27
                 177
                             BEG
                                    LRUD4
DOC7 A9 7F
DOC9 25 30
                 178
                             LDA
                                    #$7F FOR EX-OR INTO SCREEN MEM
                 179
                             AND
                                    HMASK
DOCB 31 26
                 180
                             AND
                                    (HBASL), Y SCREEN BIT SET?
DOCD DO 18
DOCF EE 2A 03
                 181
                             BINE
                                    LRUD3
                 182
                             INC
                                    COLLSN
DOD2 A9 7F
                 183
                             LDA
                                    #$7F
DOD4 25 30
                 184
                             AND
                                    HMASK
DOD6 10 12
                 185
                             BPL
                                    LRUD3 ALWAYS TAKEN.
DOD8 18
                                    NO 90 DEG ROT.
                 186 LRUD1
                             CLC
DOD9 A5 51
                                    SHAPEX
                 187 LRUD2
                             LDA
DODB 29 04
                 188
                             AND
                                    ##4 IF B2=0 THEN NO PLOT.
DODD FO OF
                 189
                             BEO
                                    LRUD4
DODF B1 26
                 190
                             LDA
                                    (HBASL), Y
DOE1 45 1C
                 191
                             EOR
                                    HCOLOR1 SET HI-RES SCREEN BIT
DOE3 25 30
                 192
                             AND
                                    HMASK TO CORRESPONDING HCOLOR1
DOE5 DO 03
                 193
                                    LRUDG IF BIT OF SCREEN CHANGES
                             RIVE
D027 EE 2A 03
                 194
                             INC
                                    COLLSN THEN INCR COLLSN DETECT
DOEA 51 26
                 195 LRUD3
                             EOR
                                    (HHASL), Y
DOEC 91 26
DOFE A5 51
                 196
                                    (HBASL), Y
                             STA
                 197 LRUD4
                             LDA
                                    SHAPEX ADD GDRNT TO
DOFO 65 53
                 198
                                    QURNT SPECIFIED VECTOR
                             ADC
DOF2 29 03
                 199
                             AND
                                    ##3 AND MOVE LFT, RT,
                 200 EQ3
                             EQU
                                    #-1 UP, OR DWN BASED
DOF4 C9 02
                 201
                                    #$2 ON SIGN AND CARRY.
                             CHP
DOF6 6A
                 202
                             ROR
DOF7 BO 8F
                 203 LRUD
                             BCS
                                   LFTRT
                 204 UPDWN
DOF9 30 30
                             BMI .
                                   DOWN4 SIGN FOR UP/DWN SELECT
DOF8 18
                 205 UP
                             CLC
DOFC A5 27
                 206
                             LDA
                                   HBASH CALC BASE ADDRESS
DOFE 2C EA D1
                 207
                             BII
                                    EGIC (ADR OF LEFTMOST BYTE)
D101 D0 22
                 208
                                   UP4 FOR NEXT LINE UP
                             BNE
D103 06 26
                 209
                             ASL
                                   HBASL IN (HBASL, HBASH)
```

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D105 B0 1A	210	BCS	UP2 WITH 192-LINE WRAPAROUND
D107 2C F3 D0	211	BIT	E03
D10A F0 05 D10C 69 1F	212 213	BEG	UP1
	214	ADC SEC	#\$1F **** BIT MAP ****
D10F B0 12			UP3 FOR ROW = ABCDEFGH,
D111 69 23	215 216 UP1	ADC	#\$23
D113 48	217	PHA	
D114 A5 26	218	LDA	HBASL HBASL = EABABOOO
D116 69 BO D118 BO 02	219 220	ADC	#\$BO HBASH = PPPFGHCD
D11A 69 FO	221	BCS ADC	UP5 ##FO WHERE PPP=001 FOR PRIMARY
	222 UP5		HBASL HI-RES PAGE (\$2000-\$3FFF)
D11E 68	223	PLA	
D11F B0 02	224 225 UP2	BCS	UP3
D121 69 1F	225 UP2		#\$1F
D123 66 26 D125 69 FC	227 UP4	ROR ADC	MBASL ##FC
D127 85 27	278 UPDWN1		HBASH
D129 60	229	RTS	
D12A 18	230 DOWN		
D12B A5 27 D12D 69 04	231 DOWN4		HBASH
DIZD 67 04	232 504	ADC	##4 CALC BASE ADR FOR NEXT LINE #-1 DOWN TO (HBASL, HBASH)
D12F 2C EA D1	234	RIT	EQ1C
D132 DO F3	235	BIT	UPDWN1
D134 06 26	236	ASL	HBASL WITH 192-LINE WRAPAROUND
D136 90 19 D138 69 E0	237 238	BCC	DOWN1
D13A 18	239	ADC CLC	#\$E0
D13B 2C 2E D1	240	BIT	EQ4
D13E FO 13	241	BEG	DOWN2
D140 A5 26	242	LDA	HBASL
D142 69 50 D144 49 FO	243	ADC	#\$50
D146 FO 02	244 245	EOR BEQ	#\$FO DOWN3
D148 49 FO	246	EOR	##F0
D14A 85 26	247 DOWN3	STA	HBASL
D14C AD 26 03	248	LDA	HPAQ
D14F 90 02 D151 69 E0	249	BCC	DOWN2
D153 66 26	250 DOWN1 251 DOWN2		##EO
D155 90 D0	252	BCC	HBASL UPDWN1
D157 48	254 * HI-1 255 HLINRL	RES GR	APHICS LINE DRAW SUBRS
D158 A9 00	256	LDA	##A CET /VAL VALLA AND
D15A 8D 20 03	257	STA	##O SET (XOL,XOH) AND XOL YO TO ZERO FOR
D15D 8D 21 03	258	STA	XOH REL LINE DRAW
D160 8D 22 03	259	STA	YO (DX, DY).
D163 68	260	PLA	- 14
D164 48 D165 38	261 HLIN 262	PHA	ON ENTRY
D166 ED 20 03	263	SEC SBC	XL: A-REG XOL XH; X-REG
D169 48	264	PHA	Y: Y-REG
D16A 8A	265	TXA	·· · · · · · · · · · · · · · · · · · ·
D16B ED 21 03	266	SBC	XOH
D16E 85 53	267	STA	QDRNT CALC ABS(X-XO)
D170 BO OA	268	BCS	HLIN2 IN (DXL, DXH).

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D172 68		PLA	
D173 49 FF		EOR	#SFF X DIR TO SIGN BIT
D175 69 01		ADC	##1 OF QDRNT.
D177 48 D178 A9 00		PHA LDA	O=RIGHT (DX POS) #\$0 1=LFFT (DX NEG)
D17A E5 53		SBC	QDRNT
D17C 85 51		STA	DXH
D17E 85 55		STA	EH INIT (EL, EH) TO
D180 48 D181 85 50		PLA	ARB(X-XO)
D183 85 54		STA STA	DXL EL
D185 68		PLA	
D186 8D 20 03		STA	XOL
D189 8E 21 03		STX	хон
D19C 98 D19D 18		TYA CLC	
D18E ED 22 03		SBC	YO CALC -ABS(Y-0)-1
D191 90 04		BCC	HLING IN DY.
D193 49 FF		EOR	#\$FF
D195 69 FE D197 85 52		ADC Sta	##FE DY ROTATE Y DIR INTO
D199 8C 22 03		STY	YO GDRNT SIGN BIT
D19C 66 53		ROR	QDRNT (O=UP, 1=DOWN)
D19E 38 D19F E5 50		SEC	DVI INIT (COUNTY COUNTY)
DIAI AA		SBC Tax	DXL INIT (COUNTL, COUNTH). TO -(DELTX+DELTY+1)
DIA2 A9 FF		LDA	##FF
D1A4 E5 51		SBC	DXH
D1A6 85 1D		STA	COUNTH HORIZ INDEX
D1A8 AC 25 03 D1A8 BO 05		LDY BCS	HNDX HORIZ INDEX MOVEX2 ALWAYS TAKEN.
DIAD OA		ASL	; MOVE IN X-DIR. USE
D1AE 20 88 DO		JSR	LFTRT GDRNT B6 FOR LFT/RT SELECT
D181 38 D182 A5 54	303 MDVEX2	SEC	EL ASSUME CARRY SET.
D184 65 52		ADC	DY (EL,EH)-DELTY TO (EL,EH)
D186 85 54		STA	EL NOTE: DY IS (-DELTY)-1
D188 A5 55		LDA	EH CARRY CLR IF (EL, EH)
D19A E9 00 D1BC 85 55	307 308 HCOUNT	SBC	#\$O GDES NEG.
D1BE B1 26		LDA	(HRASL), Y SCREEN BYTE.
D1CO 45 1C		EOR	HCOLOR1 PLOT DOT OF HCOLOR1.
D1C2 25 30		AND EOR	HMASK CURRENT BIT MASK. (HRASL), Y
D1C4 51 26 D1C6 91 26		STA	(HBASL), Y
Dice E8		INX	DONE (DELTX+DELTY)
D1C9 DO 04		BNIE	HLIN4 DOTS?
D1CB E6 1D D1CD FO 6B	316 317	INC BEG	COUNTH RTS2 YES, RETURN.
D1CF A5 53		LDA	QDRNT FOR DIRECTION TEST
DID1 BO DA		BCS	MOVEX IF CAR SET, (EL, EH) POS
D1D3 20 F9 D0	320	JSR	UPDWN IF CLR, NEG, MOVE YDIR
D1D6 18 D1D7 A5 54	-	CLC LDA	EL (EL, EH)+DELTX
D1D9 65 50	323	ADC	DXL TO (EL, EH).
D1DB 85 54	324	STA	EL
D1DD A5 55	325 326	LDA ADC	EH CAR SET IF (EL,EH) QUES POS
D1DF 65 51 D1E1 50 D9	326 327	BVC	HCOUNT ALWAYS TAKEN.
D1E3 81	328 MSKTBL	HEX	81 LEFTMOST BIT OF BYTE.
D1E4 82 84 88	329	HEX	82, 84, 88
D1E7 90 A0 D1E9 CO	330 331	HEX	90,A0 CO RIGHTMOST BIT OF BYTE.
DIEA 1C	332 E01C	HEX	1C
DIEB FF FE FA	333 COS	HEX .	FF, FE, FA, F4, EC, E1, D4, C5, B4
D1F4 A1 8D 78	334	HEX	A1, 8D, 78, 61, 49, 31, 18, FF

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HI-RES GRAPHICS COORDINATE RESTORE SUBR
                  336 *
D1FC A5 26
                  337 HFIND LDA
                                    HBASL
DIFE OA
                  338
                             ASL
                                    ; CONVERTS BASE ADR
D1FF A5 27
                  339
                             LDA
                                    HBASH TO Y-COORD.
D201 29 03
                  340
                             AND
D203 2A
                  341
                             ROL
                                    : FOR HBASL = EABABOOD
D204 05 26
                  342
                             ORA
                                    HBASL HBASH = PPPFCHCD
D509 0V
                  343
                             ASL
D207 0A
                  344
                             ASL
                                    : GENERATE
D208 0A
                  345
                             ASL
                                    ; Y-COORD = ABCDEFGH
D209 BD 22 03
                  346
                             STA
                                    YO
D20C A5 27
                  347
                             LDA
                                    HBASH (PPP=SCREEN PAGE,
D20E 4A
                  348
                             LSR
                                    ; NORMALLY 001 FOR
D20F 4A
                  349
                             LSR
                                    ; $2000-$3FFF
D210 29 07
                 350
                             AND
                                    ##7 HI-RES SCREEN)
D212 OD 22 03
                  351
                             ORA
                                    YO
D215 8D 22 03
                 352
                             STA
                                    YO CONVERTS HNDX (INDEX
D218 AD 25 03
                 353
                             LDA
                                    HNDX FROM BASE ADR)
D21B 0A
                  354
                             ABL
                                    ; AND HMASK (BIT
D21C 6D 25 03
                 355
                             ADC
                                    HNDX MASK) TO X-COORD
D21F 0A
                 356
                             ASL
                                    ; IN (XOL, XOH)
D220 AA
                  357
                             TAX
                                    (RANGE $0-$133)
D221 CA
                 358
                             DEX
D222 A5 30
                 359
                             LDA
                                    HMASK
D224 29
        7F
                 360
                             AND
                                    #$7F
D276 E8
D227 4A
                 361 HFIND1 INX
                 362
                             LSR
D228 DO FC
                 363
                             DINE
                                    HFIND1
D22A 8D 21 03
                 364
                             STA
                                    XOH
D22D 8A
                 365
                             TXA
D22E 18
                 366
                             CLC
                                    CALC HNDX+7 +
D22F 6D 25 03
                 367
                             ADC
                                   HNDX LOG (BASE 2) HMASK.
D232 90 03
                 368
                             BCC
                                    HFIND2
D234 EE 21 03
                 369
                             INC
                                    XOH
D237 8D 20 03
                 370 HFIND2 STA
                                    XOL
D23A 60
                 371 RTS2
                 373 * HI-RES GRAPHICS SHAPE DRAW SUBR
                 374 *
                 375 * SHAPE DRAW
                 376 # R = 0 TO 63
                 377 * SCALF FACTOR USED (1=NORMAL)
                 378 *
D238 86 1A
                 379 DRAW
                             STX
                                   SHAPE: DRAW DEFINITION
D23D 84 1B
                 380
                             STY
                                   SHAPEH POINTER.
D23F AA
                 381 DRAW1
                             TAX
D240 4A
                 382
                             LSR
                                   ; ROT ($0-$3F)
D241 4A
                 383
                             LSR
D242 4A
                 384
                             LSR
                                   ; GDRNT O=UP, 1=RT,
D243 4A
                 385
                             LSR
                                   ; 2=DWN, 3=LFT.
D244 85 53
                 386
                             STA
                                   QDRNT
D246 BA
                 387
                             TXA
D247 29 OF
                 388
                             AND
                                   #$F
D249 AA
                 389
                             TAX
D24A BC EB D1
                 390
                             LDY
                                   COS, X SAVE COS AND SIN
D24D 84 50
                 391
                             STY
                                   DXL VALS IN DXL AND DY.
D24F 49 OF
                 392
                             EOR
                                   ##F
D251 AA
                 393
                             TAX
D252 BC EC D1
                 394
                             LDY
                                   COS+1, X
D255 C8
                 395
                             INY
D256 84 52
                 396
                             STY
D258 AC 25 03
                 397 DRAW2
                             LDY
                                   HNDX BYTE INDEX FROM
D25B A2 00
                 398
                             LDX
                                   ##0 HI-RES BASE ADR.
D25D 8E 2A 03
                 399
                             STX
                                   COLLSN CLEAR COLLISION COUNT.
D260 A1 1A
                 400
                            LDA
                                   (SHAPEL, X) 1ST SHAPE DEF BYTE.
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D262 85 51	401 DRAW3 STA	SHAPEX
D264 A2 80	402 LDX	#\$80
D266 86 54 D268 86 55	403 STX 404 STX	EI. EL, EH FOR FRACTIONAL EH L, R, U, D VECTORS.
D26A AE 27 03		SCALE SCALE FACTOR.
D26D A5 54	406 DRAW4 LDA	EL
D26F 38	407 SEC	IF FRAC COS OVFL
D270 65 50	408 ADC	DXL THEN MOVE IN
D272 85 54	409 STA	EL SPECIFIED VECTOR
D274 90 04	410 BCC	DRAWS DIRECTION.
D276 20 D8 D0	411 JSR	LRUD1
D2/9 18	412 CLC	
D27A A5 55	413 DRAWS LDA	EH IF FRAC BIN OVFL
D27C 65 52	414 ADC	DY THEN MOVE IN
D27E 85 55	415 STA	EM SPECIFIED VECTOR
D230 90 03 D232 20 D9 D0	416 BCC 417 JSR	DRAW6 DIRECTION +90 DEG. LRUD2
D235 CA	418 DRAW6 DEX	LOOP ON SCALE
D286 DO E5	419 BNE	DRAW4 FACTOR.
D238 A5 51	420 LDA	SHAPEX
D23A 4A	421 LSR	; NEXT 3-BIT VECTOR
D28B 4A	422 LSR	; OF SHAPE DEF.
D28C 4A	423 LSR	
D23D DO D3	424 BNE	DRAWS NOT DONE THIS BYTE.
D23F E6 1A	425 INC	SHAPEL
D271 DO 02	426 BNE	DRAW7 NEXT BYTE OF
	427 INC	SHAPEH SHAPE DEFINITION.
D275 A1 1A	428 DRAW7 LDA	(SHAPEL, X)
D277 DO C9 D279 60	429 BNE 430 RTS	DRAW3 DONE IF ZERO.
D277 GO	430 K10	
	432 * HI-RES (GRAPHICS SHAPE EX-OR SUBR
		APE INTO SCREEN.
		TO 3 (QUADRANT ONLY)
	437 * SCALE IS	
	438 *	
D29A 86 1A	439 XDRAW STX	
D29C 84 1B	440 STY	
D29E AA	441 XDRAW1 TAX	
D29F 4A D2AO 4A	442 LSR	
D2A1 4A	443 LSR 444 LSR	; GDRNT O=UP, 1=RT,
D2A2 4A	445 LSR	
D2A3 85 53	446 STA	
D2A5 BA	447 TXA	
D2A6 29 OF	448 AND	
D2A8 AA	449 TAX	
D2A9 BC EB D1	450 LDY	
D2AC 84 50	451 STY	
D2AE 49 OF	452 EOR	
D2BO AA D2B1 BC EC D1	453 TAX	
D281 BC EC D1	454 LDY 455 INY	
D285 84 52	455 INT	
D287 AC 25 03	457 XDRAW2 LDY	
D28A A2 00	458 LDX	
D28C 8E 2A 03	459 STX	
D28F A1 1A	460 LDA	

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D2C3 A2 80	Apple 2 Technical Manual	Programmer's Aid # 11978
D2C3 A2 80		
D2C5 86 54		- · · · - · · · · · · · · · · · · · · ·
D2C7 86 55		
D2C9 AE 27 03 465 LDX D2CC 38 54 466 XDRAM LDA EL D2CC 28 55 0 469 STA D2D1 85 54 469 STA D2D2 18 470 SCC D2D5 20 C0 D0 471 JSR D2D8 18 472 CLC D2D9 A5 55 473 XDRAM5 LDA D2D8 65 52 474 D2DD 85 55 475 STA D2D8 65 52 474 D2DD 85 55 475 STA D2D8 67 50 477 JSR D2D8 67 50 477 JSR D2F7 40 03 476 SCC D2F7 20 DP D0 477 JSR D2F8 50 C5 D2F7 20 DP D0 477 JSR D2F8 60 490 LDA D2F8 60 15 479 SNE D2F8 40 489 LSR D2F8 40 489 LSR D2F8 40 489 LSR D2F8 41 489 LSR D2F8 41 489 LSR D2F8 60 DD 20 489 SNE D2F9 20 DD 24 489 SNE D2F8 20 DC 29 489 SNE D2F8 20 DC 29 489 SNE D2F9 20 DD 20 479 SNE D2F9 20 DD 20 479 SNE D2F9 20 DD 20 489 SNE D2F9 20 DD 20 50 SNE D2F9 20 DD 20		STX EL EL, EH FOR FRACTIONAL
D2CC AS 54		STX EH L.R.U.D. VECTORS.
D2CE 38 D2CF 45 50 469 D2D1 85 54 469 D2D1 85 55 D2D1 85 55 D2D1 85 52 D2D1 85 55 D		
D2CF 65 50		
D2D1 85 54 469 STA EL SPECIFIED VECTOR D2D5 20 CD D0 471 JSR LEUDX1 D2D8 18 472 D2D9 A5 55 473 XDRAH5 LDA D2D8 65 52 474 D2DD 85 55 475 STA EH SPECIFIED VECTOR D2D8 55 52 475 STA EH SPECIFIED VECTOR D2D8 55 475 STA EH SPECIFIED VECTOR D2F1 20 DP D0 477 JSR LBUD2 D2F2 60 CS A78 XDRAH5 DEX LDOP ON SCALE D2E5 D0 E5 479 SNE XDRAH5 DEX LDOP ON SCALE D2E5 D0 E5 479 SNE XDRAH5 AFACTOR. D2E4 4A 481 LSR ; NEXT 3-BIT VECTOR D2F2 A4 481 LSR ; NEXT 3-BIT VECTOR D2F2 D0 D0 344 BNE XDRAH2 D2F2 D0 D0 484 BNE XDRAH2 D2F2 E6 IA 485 INC SHAPEL D2F3 D0 C9 486 BNE XDRAH3 DONE IF ZERO. D2F4 A4 IA 488 XDRAH7 D2F4 A4 IA A88 XDRAH7 D2F4 B0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F5 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D2F6 D0 C9 489 SNE XDRAH3 DONE IF ZERO. D301 40 C3 03 SNO LDX BXSAV D302 40 A95 SNE		SEC IF FRAC COS OVFL
D2P3 90 04 470 BCC XDRAWS DIRECTION D2P3 20 CC DO 471 JSR D2D8 18 472 CLC LRUDXI D2P4 A5 55 473 XDRAWS LDA D2D8 45 55 473 XDRAWS LDA D2 D2P5 55 475 STA D2D1P5 90 03 476 BCC XDRAWS D2P4 PA D2P5 20 DP D0 477 JSR D2P4 A5 51 480 LDA SHAPEX D2P4 A6 481 LSR , NEXT 3-BIT VECTOR D2P4 A6 482 LSR , NEXT 3-BIT VECTOR D2P4 A6 483 LSR D2P6 DD D3 484 BNE D2P6 DD D3 484 BNE D2P6 DD D3 484 BNE D2P6 DD D4 486 BNE XDRAWS D2P6 DD D4 489 NEXT XDRAWS D2P6 DD C9 489 BNE XDRAWS D2P6 DD C9 489 BNE XDRAWS D2P6 DD C9 489 BNE XDRAWS D2P6 DD C9 489 D2P6 BO C9 50 D2P6 BO C9		ADC DXL THEN MOVE IN
D2D5 20 CD D0 471		STA EL SPECIFIED VECTOR
Day		BCC XDRAWS DIRECTION
D2D9 A5 55	III TE TE TE TE	JSR LRUDX1
D2DB 65 52 474 ADC DY THEN MOVE IN D2DB 85 55 475 STA EN SPECIFIED VECTOR D2DB 55 55 475 STA EN SPECIFIED VECTOR D2F1 20 D9 D0 477 JSR LRUD2 D2F1 20 D9 D0 477 JSR LRUD2 D2F2 CA 478 XDRAM6 DEX LODP ON SCALE D2F3 D0 E5 477 SHE XDRAM6 FACTOR. D2F7 A5 51 480 LDA SHAPEX D2F2 4A 481 LSR ; NEXT 3-BIT VECTOR D2F8 4A 482 LSR ; NEXT 3-BIT VECTOR D2F8 4A 483 LSR D2FC D0 D3 484 SHE XDRAM7 DF SHAPEL D2FB 4A 489 LSR D2FC D0 D3 484 SHE XDRAM7 DEXT BYTE OF D2FC E6 1A 489 INC SHAPEL SHAPE DEF. D2FC D0 O2 486 SHE XDRAM7 LDA (SHAPEL SHAPE DEF. D2F2 E6 1B 487 INC SHAPEL SHAPE DEF. D2F4 A1 1A 488 XDRAM7 LDA (SHAPEL SHAPE DEF. D2F6 D0 C9 489 SHE XDRAM3 DONE IF ZERO. D2F6 D0 C9 489 SHE XDRAM3 DONE IF ZERO. D2F6 D0 C9 489 SHE XDRAM3 DONE IF ZERO. D2F6 D0 C9 489 SHE XDRAM3 DONE IF ZERO. D2F7 20 90 D3 473 SPOSN JSR PCOLR POSN CALL. COLR FROM BASIC D2F6 20 AF D3 475 JSR GETVO YO FROM BASIC. D300 48 499 PLA D301 20 9A D3 497 JSR SHE CETVO YO FROM BASIC. D302 48 496 PHA D303 20 9A D3 497 JSR SETVO YO FROM BASIC. D301 40 25 D3 504 BLINI LDA HNDX D301 40 25 D3 504 BLINI LDA HNDX D301 40 25 D3 504 BLINI LDA HNDX D301 40 50 501 JR P SPOSN PLOT CALL (BASIC). D301 20 75 D0 507 JSR HPOSN D318 20 75 D0 507 JSR HPOSN D318 20 75 D0 507 JSR HPOSN D329 A8 511 TYA D302 48 510 PHA D303 48 510 PHA D303 48 511 TYA D304 A8 512 TAX D305 48 515 PLA D307 AE 23 D3 517 LDX BXSAV D309 60 518 STS PLOUR BASIC D309 60 519 SPON JSR PCOLR BASIC VAR COLR. D309 60 510 STS PLOUR BASIC D309 60 511 TYA D309 60 512 TAX D309 60 513 STS PLOUR BASIC D309 60 514 STS PLOUR BASIC D309 60 515 STS PLA D309 60 516 STS PLA D309 60 517 TAX D309 60 518 STS PLOUR BASIC D309 60 519 SPON JSR PCOLR BASIC VAR COLR. D309 60 519 SPON JSR PCOLR BASIC VAR COLR. D309 60 519 SPON JSR PCOLR BASIC VAR COLR. D309 60 519 SPON JSR PCOLR BASIC VAR COLR. D309 60 519 SPON JSR PCOLR BASIC VAR COLR. D309 60 519 SPON JSR PCOLR BASIC VAR COLR. D309 60 519 SPON JSR PCOLR BASIC VAR COLR. D309 60 519 SPON JSR PCOLR BACKGROUND CALL	_	
D2DD 85 55		
D201F 90 03		
D2F1 20 D9 D0 477 JSR LRUDZ D2E4 CA 478 XDRAM6 DEX LOOP ON SCALE D2E5 D0 E5 479 BNE XDRAM6 PEX D2E7 A5 51 480 LDA SHAPEX D2E7 A5 51 480 LDA SHAPEX D2EA 4A 481 LSR , NEXT 3-BIT VECTOR D2EA 4A 482 LSR , DF SHAPE DEF. D2EA 4A 483 LSR D2EC D0 D3 484 BNE XDRAM3 D2EE 61 A 485 INC SHAPEL D2F0 D0 02 486 BNE XDRAM3 D2EE 61 A 485 INC SHAPEL BYTE OF D2F2 61 B 487 INC SHAPEL BABPE DEF. D2F3 60 490 RTS D2F4 A1 1A 488 XDRAM7 LDA (SHAPEL, X) D2F6 D0 C9 489 BNE XDRAM3 DONE IF ZERO. D2F7 20 90 D3 493 BPOSN JSR PCOLR POSN CALL, COLR FROM BASIC D2FF 8D 24 03 494 STA D2F6 D0 C9 489 JSR PCOLR POSN CALL, COLR FROM BASIC D2FF 20 AF D3 495 JSR GETVO YO FROM BASIC. D302 48 496 PHA D303 20 9A D3 497 JSR GETVO YO FROM BASIC. D303 20 9A D3 497 JSR HPOSN D304 AE 23 03 500 LDX BXSAV D305 20 FP D2 502 BPLOT JSR BYDSN PLOT CALL (BASIC). D311 4C 7D D0 503 JMP HPLOT1 D314 AD 25 03 504 BLINI LDA HNDX D317 4A 505 LSR SET COLORI FROM D318 20 9A D3 506 JSR PCOLR BASIC VAR COLR. D318 20 9A D3 508 BLINE JSR GETYO YO FROM BASIC D319 24 8 505 JSR PLOT JSR BYDSN PLOT CALL (BASIC). D319 24 8 505 JSR PLOT JSR BYDSN PLOT CALL (BASIC). D319 24 8 505 JSR PLOT JSR BYDSN PLOT CALL (BASIC). D319 25 20 AF D3 513 JSR GETYO YO FROM BASIC D328 48 510 PHA D329 48 511 TYA D329 48 512 TAX D329 48 511 JYA D329 48 512 TAX D329 48 514 TAY D329 48 515 PLA D329 60 40 11 516 JSR HLIN D320 60 518 BND JSR PCOLR BASIC VAR COUR. D320 60 518 SNSAV D330 60 519 BOND JSR PCOLR BASIC D320 60 518 SNSAV D330 60 519 BOND JSR PCOLR BASIC D320 60 61 518 SNSAV D330 60 510 SNSAV D330 60 5		STA EH SPECIFIED VECTOR
D264 CA 478 XDRAM6 DEX LODP ON SCALE D265 D0 E5 479 BNE XDRAM4 FACTOR. D267 A5 51 480 LDA D262 44 481 LSR ; NEXT 3-BIT VECTOR D268 44 483 LSR D276 D0 D3 484 BNE XDRAM7 LDA D264 E6 1A 485 INC D269 E6 1B 486 INC D269 E6 1B 487 INC D269 E6 1B 488 XDRAM7 LDA D264 E7 1B 488 XDRAM7 LDA D264 E7 1B 488 XDRAM7 LDA D266 E7 1B 488 XDRAM7 LDA D267 E7 1B 488 XDRAM7 LDA D267 E7 1B 488 XDRAM7 LDA D268 E7 1B 488 XDRAM7 L		- · · · · · · · · · · · · · · · ·
D2E5 D0 E5		
D2F7 45 51		
D2E9 4A		BNE XDRAW4 FACTOR.
D2:A 4A 482 LSR		LDA SHAPEX
D278 4A 483 LSR D276 D0 D3 484 BINE XDRAM3 D276 E6 1A 485 INC SHAPEL D270 D0 02 486 BINE XDRAM7 NEXT BYTE OF D272 E6 1B 487 INC SHAPEL SHAPE DEF. D274 A1 1A 488 XDRAM7 LDA (SHAPEL, X) D276 D0 C9 489 BINE XDRAM3 DONE IF ZERO. D276 D0 C9 489 BINE XDRAM3 DONE IF ZERO. D276 D0 C9 489 BINE XDRAM3 DONE IF ZERO. D276 BD 24 03 494 STA HCOLOR D276 BD 24 03 495 JSR PCOLR POSN CALL, COLR FROM BASIC D276 BD 24 03 495 JSR GETYO YO FROM BASIC. D276 BD 24 03 495 JSR GETYO YO FROM BASIC. D276 BD 24 03 497 JSR GETYO YO FROM BASIC. D276 BD 24 03 497 JSR GETYO YO FROM BASIC. D276 BD 300 AB 23 03 500 LDX BXSAV D303 A0 AE 23 03 500 LDX BXSAV D303 A0 AE 23 03 500 LDX BXSAV D304 AB D307 20 ZE D0 499 JSR BPOSN PLOT CALL (BASIC). D311 4C 7D D0 503 JMP HPLOT1 D314 A0 Z5 03 504 BLINI LDA HNDX D317 4A 505 JSR PCOLR BASIC VAR COLR. D318 20 70 D3 504 BLINI LDA HNDX D317 4A 505 JSR PCOLR BASIC VAR COLR. D318 20 70 D3 506 JSR PCOLR BASIC VAR COLR. D318 20 70 D3 506 BLINE JSR PCOLR BASIC VAR COLR. D322 48 510 PHA D323 40 A5 12 TAX D322 48 510 PHA D323 A8 511 TYA D324 AA 512 TAX D325 20 AF D3 513 JSR GETYO YO FROM BASIC D329 AB 511 TYA D320 AE 23 03 517 LDX BXSAV D321 20 70 D3 519 BOND JSR PCOLR BACKGROUND CALL		LSR ; NEXT 3-BIT VECTOR
D2EC DO D3		LSR ; OF SHAPE DEF.
D2FE E6 1A		LSR
D2F-0 D0 O2		BNE XDRAW3
D2F2 E6 1B		INC SHAPEL
D2F4 A1 1A		BHE XDRAW7 NEXT BYTE OF
D2F6 D0 C9		
D2F8 60		
## ENTRY POINTS FROM APPLE-II BASIC ## ENTRY POINTS FROM APPLE-II BASIC ##		
D2F7 20 90 D3 493 BPOSN JSR PCOLR POSN CALL, COLR FROM BASIC D2FF 20 AF D3 495 JSR PCTYO YO FROM BASIC. D302 48 496 PHA D303 20 9A D3 497 JSR PLA D307 20 2E D0 499 JSR PLA D30A AE 23 03 500 LDX BXSAV D30B 20 F9 D2 502 BPLOT JSR BPOSN PLOT CALL (BASIC). D311 4C 7D D0 503 JMP HPLOT1 D314 AD 25 03 504 BLIN1 LDA D317 4A 505 LSR SET HCOLOR1 FROM D318 20 90 D3 506 JSR PCOLR BASIC VAR COLR. D318 20 90 D3 508 BLINE JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D320 48 510 PHA D323 98 511 TYA D324 AA 512 TAX D325 20 AF D3 513 JSR GETYO YO FROM BASIC D328 AB 514 TAY D329 68 515 PLA D320 AE 23 03 517 LDX BXSAV D331 20 90 D3 519 BOND JSR PCOLR BACKGROUND CALL	D2F8 60 490	RTS
D2F7 20 90 D3 493 BPOSN JSR PCOLR POSN CALL, COLR FROM BASIC D2FF 20 AF D3 495 JSR PCTYO YO FROM BASIC. D302 48 496 PHA D303 20 9A D3 497 JSR PLA D307 20 2E D0 499 JSR PLA D30A AE 23 03 500 LDX BXSAV D30B 20 F9 D2 502 BPLOT JSR BPOSN PLOT CALL (BASIC). D311 4C 7D D0 503 JMP HPLOT1 D314 AD 25 03 504 BLIN1 LDA D317 4A 505 LSR SET HCOLOR1 FROM D318 20 90 D3 506 JSR PCOLR BASIC VAR COLR. D318 20 90 D3 508 BLINE JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D319 20 75 D0 507 JSR PCOLR BASIC VAR COLR. D320 48 510 PHA D323 98 511 TYA D324 AA 512 TAX D325 20 AF D3 513 JSR GETYO YO FROM BASIC D328 AB 514 TAY D329 68 515 PLA D320 AE 23 03 517 LDX BXSAV D331 20 90 D3 519 BOND JSR PCOLR BACKGROUND CALL	400 # ENTO	V BOINTS COOM ADDIC-II BAGIC
D2FC 9D 24 03 494 STA HCOLOR D2FF 20 AF D3 495 JSR GETYO YO FROM BASIC. D302 48 496 PHA D303 20 9A D3 497 JSR GETXO XO FROM BASIC. D306 68 498 PLA D307 20 2E D0 499 JSR HPOSN D30A AE 23 03 500 LDX BXSAV D30D 60 501 RTS D30E 20 F9 D2 502 BPLOT JSR BPOSN PLOT CALL (BASIC). D311 4C 7D D0 503 JMP HPLOT1 D314 AD 25 03 504 BLIN1 LDA HNDX D317 4A 505 LSR ; SET HCOLOR1 FROM D318 20 90 D3 506 JSR PCOLR BASIC VAR COLR. D318 20 9A D3 508 BLINE JSR GETYO LINE CALL, GET XO FROM BASIC D321 8A 509 TXA D322 48 510 PHA D323 98 511 TYA D324 AA 512 TAX D325 20 AF D3 513 JSR GETYO YO FROM BASIC D328 AB 514 TAY D329 68 515 PLA D329 68 515 PLA D329 68 515 PLA D320 60 518 RTS D331 20 90 D3 519 BGND JSR PCOLR BACKGROUND CALL		
D2FF 20 AF D3 495		
D302 48		
D303 20 9A D3 497 JSR GETXO XO FROM BASIC. D306 68 498 PLA D307 20 2E D0 499 JSR HPOSN D30A AE 23 03 500 LDX BXSAV D30D 60 501 RTS D30E 20 F9 D2 502 BPLOT JSR BPOSN PLOT CALL (BASIC). D311 4C 7D D0 503 JMP HPLOT1 D314 AD 25 03 504 BLIN1 LDA HNDX D317 4A 505 LSR ; SET HCOLOR1 FROM D318 20 90 D3 506 JSR PCOLR BASIC VAR COLR. D318 20 90 D3 506 JSR PCOLR BASIC VAR COLR. D318 20 9A D3 508 BLINE JSR GETXO LINE CALL, GET XO FROM BASIC D321 BA 509 TXA D322 48 510 PHA D323 98 511 TYA D324 AA 512 TAX D325 20 AF D3 513 JSR GETYO YO FROM BASIC D329 AB 514 TAY D329 AB 515 PLA D320 60 518 RTS D331 20 90 D3 519 BGND JSR PCOLR BACKGROUND CALL		
D396 68		
D307 20 2E D0 499		
D30A AE 23 03 500		
D30D 60	D30A AE 23 03 500	
D30E 20 F9 D2 502 BPLOT JSR BPOSN PLOT CALL (BASIC). D311 4C 7D D0 503 JMP HPLOT1 D314 AD 25 03 504 BLIN1 LDA HNDX D317 4A 505 LSR ; SET HCOLOR1 FROM D318 20 70 D3 506 JSR PCOLR BASIC VAR COLR. D318 20 75 D0 507 JSR HPOSN3 D31E 20 9A D3 508 BLINE JSR GETXO LINE CALL, GET XO FROM BASIC D321 8A 509 TXA D322 48 510 PHA D323 98 511 TYA D324 AA 512 TAX D325 20 AF D3 513 JSR GETYO YO FROM BASIC D328 AB 514 TAY D329 68 515 PLA D329 68 515 PLA D320 AE 23 03 517 LDX BXSAV D330 60 518 RTS D331 20 90 D3 519 BGND JSR PCOLR BACKGROUND CALL		
D311 4C 7D D0 503 JMP HPLOT1 D314 AD 25 03 504 BLIN1 LDA HNDX D317 4A 505 LSR ; SET HCOLOR1 FROM D318 20 90 D3 506 JSR PCOLR BASIC VAR COLR. D318 20 75 D0 507 JSR HPOSN3 D318 20 9A D3 508 BLINE JSR GETXO LINE CALL, GET XO FROM BASIC D321 BA 509 TXA D322 48 510 PHA D323 98 511 TYA D324 AA 512 TAX D325 20 AF D3 513 JSR GETYO YO FROM BASIC D328 AB 514 TAY D329 68 515 PLA D320 AE 23 03 517 LDX BXSAV D330 60 518 RTS D331 20 90 D3 519 BGND JSR PCOLR BACKGROUND CALL		
D314 AD 25 03 504 BLIN1 LDA HNDX D317 4A 505 LSR ; SET HCOLOR1 FROM D318 20 90 D3 506 JSR PCOLR BASIC VAR COLR. D318 20 75 D0 507 JSR HPOSN3 D312 20 9A D3 508 BLINE JSR GETXO LINE CALL, GET XO FROM BASIC D321 BA 509 TXA D322 48 510 PHA D323 98 511 TYA D324 AA 512 TAX D325 20 AF D3 513 JSR GETYO YO FROM BASIC D328 AB 514 TAY D329 68 515 PLA D320 AE 23 03 517 LDX BXSAV D330 60 518 RTS D331 20 90 D3 519 BGND JSR PCOLR BACKGROUND CALL		
D317 4A		
D318 20 90 D3 506		
D31B 20 75 D0 507		
D31E 20 9A D3 508 BLINE JSR GETXO LINE CALL, GET XO FROM BASIC D321 8A 509 TXA D322 48 510 PHA D323 98 511 TYA D324 AA 512 TAX D325 20 AF D3 513 JSR GETYO YO FROM BASIC D328 AB 514 TAY D329 6B 515 PLA D329 6B 515 PLA D320 AE 23 03 517 LDX BXSAV D330 60 518 RTS D331 20 90 D3 519 BGND JSR PCOLR BACKGROUND CALL		
D321 8A 509 TXA D322 48 510 PHA D323 98 511 TYA D324 AA 512 TAX D325 20 AF D3 513 JSR GETYO YO FROM BASIC D328 AB 514 TAY D329 6B 515 PLA D32A 20 64 D1 516 JSR HLIN D32D AE 23 03 517 LDX BXSAV D330 60 518 RTS D331 20 90 D3 519 BGND JSR PCOLR BACKGROUND CALL		
D322 48 510 PHA D323 98 511 TYA D324 AA 512 TAX D325 20 AF D3 513 JSR GETYO YO FROM BASIC D328 AB 514 TAY D329 68 515 PLA D32A 20 64 D1 516 JSR HLIN D32D AE 23 03 517 LDX BXSAV D330 60 518 RTS D331 20 90 D3 519 BGND JSR PCOLR BACKGROUND CALL		
D323 98 511 TYA D324 AA 512 TAX D325 20 AF D3 513 JSR GETYO YO FROM BASIC D328 AB 514 TAY D329 68 515 PLA D32A 20 64 D1 516 JSR HLIN D32D AE 23 03 517 LDX BXSAV D330 60 518 RTS D331 20 90 D3 519 BGND JSR PCOLR BACKGROUND CALL		
D324 AA 512 TAX D325 20 AF D3 513 JSR GETYO YO FROM BASIC D328 AB 514 TAY D329 68 515 PLA D32A 20 64 D1 516 JSR HLIN D32D AE 23 03 517 LDX BXSAV D330 60 518 RTS D331 20 90 D3 519 BGND JSR PCOLR BACKGROUND CALL		
D325 20 AF D3 513		
D328 AB 514 TAY D329 68 515 PLA D32A 20 64 D1 516 JSR HLIN D32D AE 23 03 517 LDX BXSAV D330 60 518 RTS D331 20 90 D3 519 BGND JSR PCOLR BACKGROUND CALL		
DGR9 68 515 PLA DGRA 20 64 D1 516 JSR HLIN DGRD AE 23 03 517 LDX BXSAV DGGO 518 RTS DGG1 20 90 DG 519 BOND JSR PCOLR BACKGROUND CALL		
D32A 20 64 D1 516 JSR HLIN D32D AE 23 03 517 LDX BXSAV D33O 60 518 RTS D331 20 90 D3 519 BGND JSR PCOLR BACKGROUND CALL		
D32D AE 23 03 517 LDX BXSAV D330 60 518 RTS D331 20 90 D3 519 BGND JSR PCOLR BACKGROUND CALL		
D330 60 518 RTS D331 20 90 D3 519 BGND JSR PCOLR BACKGROUND CALL		
D331 20 90 D3 519 BOND JSR PCOLR BACKGROUND CALL		
and the second s		

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522 *
                       DRAW ROUTINES
D337 20 F9 D2
                 523 BDRAW1 JSR
                                   BPOSN
D33A 20 51 D3
                 524 3DRAW
                            JSR
                                   BDRAWX DRAW CALL FROM BASIC.
D33D 20 3B D2
                 525
                            JSR
                                   DRAW
D340 AE 23 03
                 526
                            LDX
                                   BXSAV
D313 60
                 527
                            RTS
D344 20 F9 D2
                 528 BXDRW1 JSR
                                   BPOSN
D347 20 51 D3
                 529 BXDRAW JSR
                                   BDRAWX EX-OR DRAW
D34A 20 9A D2
                 530
                             JSR
                                   XDRAW FROM BASIC.
D31D AE 23 03
                 531
                            LDX
                                   BXSAV
D350 60
                 532
                            RTS
D351 BE 23 03
                 533 BDRAWX STX
                                   BXSAV SAVE FOR BASIC.
                            LDY
D354 A0 32
                                   #$32
                 534
D356 20 92 D3
                 535
                             JSR
                                   PBYTE SCALE FROM BASIC.
D359 8D 27 03
                 536
                            STA
                                   SCALE
D35C A0 28
                 537
                            LDY
                                   #$28
D35E 20 92 D3
                                   PBYTE ROT FROM BASIC.
                 538
                             JSR
D361 48
                 539
                            PHA
                                   SAVE ON STACK.
D362 AD 28 03
                                   SHAPXL
                            LDA
                 540
D355 85 1A
                 541
                             STA
                                   SHAPEL START OF
                 542
                                   SHAPXH SHAPE TABLE.
D357 AD 29 03
                            LDA
                                   SHAPEH
D36A 85 18
                 543
                             STA
D35C A0 20
                 544
                            LDY
                                   ##20
D35E 20 92 D3
                 545
                             JSR
                                   PBYTE SHAPE FROM BASIC.
D371 FO 39
                 546
                             BEG
                                   RERR1
D373 A2 00
                 547
                            LDX
                                   #40
D375 C1 1A
                 548
                             CMP
                                   (SHAPEL, X) > NUM OF SHAPES?
D377 FO 02
                             BEG
                 549
                                   BDRWX1
D3/9 BO 31
                 550
                                   REHR1 YES, RANGE ERR.
                             BCS
                 551 BDRWX1 ASL
D37B 0A
D37C 90 03
                 552
                             BCC
                                   BDRWX2
D37E E6 1B
                 553
                             INC
                                   SHAPEH
D330 18
                 554
                             CLC
                 555 BDRWX2 TAY
D331 A8
                                   SHAPE NO. * 2.
D332 B1 1A
                 556
                             LDA
                                   (SHAPEL), Y
D334 65 1A
                                   SHAPEL
                 557
                             ADC
D396 AA
                 558
                             TAX
                                   ADD 2-BYTE INDEX
D337 C8
                 559
                             INY
                                   TO SHAPE TABLE
                                   (SHAPEL), Y START ADR
D398 B1 1A
                             LDA
                 560
D33A 6D 29 03
                 561
                             ADC
                                   SHAPXH (X LOW, Y HI).
DGOD A8
                             TAY
                 562
                                   ROT FROM STACK.
DGGE 68
                 563
                             PLA
D33F 60
                             RTS
                 564
                 566 * BASIC PARAM FEICH SUBR'S
D390 A0 16
                 567 PCOLR LDY
                                   #$16
                 568 PBYTE
D392 B1 4A
                                   (LOMENL), Y
                            LDA
D394 DO 16
                 569
                             BHE
                                   RERRI GET BASIC PARAM.
D376 88
                 570
                             DEY
                                   (ERR IF >255)
D377 B1 4A
                 571
                             LDA
                                   (LOMEML), Y
D379 60
                 572 RTSB
                             RTS
D39A 8E 23 03
                 573 GETXO
                                   BXSAV SAVE FOR BASIC.
                             STX
D37D A0 05
                 574
                             LDY
                                   #$5
D39F B1 4A
                 575
                             LDA
                                   (LOMEML), Y XO LOW-ORDER BYTE.
D3A1 AA
                 576
                             TAX
D3A2 C8
                 577
                             INY
D3A3 B1 4A
                 578
                             LDA
                                   (LOMEML), Y HI-ORDER BYTE.
D3A5 A8
                 579
                             TAY
D3A6 E0 18
                 580
                             CPX
                                   #$18
D3A8 E9 01
                 581
                             SBC
                                   #$1 RANGE ERR IF >279
DGAA 90 ED
                 582
                             BCC
                                   RTSB
D3AC 4C 68 EE
                 583 RERR1
                             JMP
                                   RNGERR
DO OA AAEG
                 584 GETYO
                            LDY
                                   ##D OFFSET TO YO FROM LOMEM
D3R1 20 92 D3
                 585
                             JSR
                                   PBYTE GET BASIC PARAM YO.
D384 C9 CO
                             CHP
                 586
                                   ##CO (ERR IF >191)
D386 B0 F4
                 587
                             BCS
                                   RERR1
D388 60
                 588
                             RTS
```

```
590 * SHAPE TAPE LOAD SUBROUTINE
D3R9 BE 23 03
                  591 SHLOAD STX
                                    BXSAV SAVE FOR BASIC.
D3RC 20 1E F1
D3RF 20 FD FE
                                     ACADR READ 2-BYTE LENGTH INTO
                  592
                              JSR
                  593
                              JSR
                                    READ BASIC ACC
D3C2 A9 00
                  594
                              LDA
                                     #$00 ; START OF SHAPE TABLE IS $0800
D3C4 85 3C
                  595
                              STA
D3C6 8D 28 03
D3C9 18
                  596
                              STA
                                    SHAPXL
                  597
                              CLC
D3CA 65 CE
                  598
                              ADC
                                     ACL
DGCC AB
                  599
                              TAY
D3CD A9 08
                  600
                              LDA
                                     ##08 ; HIGH BYTE OF SHAPE TABLE POINTER.
D3CF 85 3D
                  601
                              STA
                                     A1H
D3D1 8D 29 03
                  602
                              STA
                                    SHAPXH
D3D4 65 CF
                              ADC
                  603
                                    ACH
D3D6 B0 25
D3D8 C4 CA
                  604
                              BCS
                                    MFULL1 NOT ENOUGH MEMORY.
                  605
                              CPY
                                    PPL
D3DA 48
                              PHA
                  606
D3DB E5 CB
                  607
                              SEC
                                    PPH
D3DD 68
                  608
                              PLA
                                    MFULL1
DODE BO 1D
                  609
                              BCS
D3E0 84 3E
                  610
                              STY
D3E2 85 3F
                  611
                              STA
                                     APH
D3:4 C8
                  612
                              INY
D325 D0 02
                  613
                              BHE
                                     SHI OD1
DGF.7 69 01
                  614
                              ADC
                                     ##1
D3E9 84 4A
                  615 SHLOD1 STY
                                    LOMEML
DGF.B 85 4B
                  616
                              STA
                                    LOMEMH
D3F.D 84 CC
                  617
                              STY
                                    PVL
DOLF 85 CD
                                    PVH
                  618
                              STA
D351 20 FA FC
                  619
                              JSR
                                     RD2BIT
                                    ##3 . 5 SECOND HEADER.
READX1
D354 A9 03
                  620
                              LDA
D356 20 02 FF
                  621
                              JSR
D3F9 AE 23 03
                  622
                              LDX
                                     BXSAV
D3FC 60
                  623
                              RTS
DGFD 4C 6B E3
                 624 MFULL1 JMP
                                    MEMFUL
```

--- END ASSEMBLY ---

TOTAL ERRORS: 00

```
2 *
      APPLE-JC BASIC RENUMBER / APPEND SUBROUTINES
 5 *
                     VERSION TWO
5
                       RENUMBER
                      >CLR
8 *
                      >START=
                      >STEP=
10 #
                      DCALL -10531
11 #
12 *
                       OPTIONAL
13 *
                      >FROM=
                      >T0=
14 *
15 *
                      >CALL -10521
16 *
17 *
                    USE RENX ENTRY
18 *
                   FOR RENUMBER ALL
19
                 NOZ
                        APRIL 12, 1978
20 *
                  APPLE COMPUTER INC.
21 *
22 ************
24 *
25 *
            6502 EQUATES
26 *
27 *
28 ROL
                            LOW-ORDER SW16 RO BYTE.
          EQU
                $0
29 ROH
          EQU
                $1
                            HI-CRDER.
30 ONE
          EQU
                $01
                            LOW-ORDER SW16 R11 BYTE.
31 R11L
          EQU
                $16
32 R11H
          EQU
               $17
                             HI-ORDER.
               ≠CA
$CC
$E<sup>-2</sup>
                            BASIC HIMEM POINTER.
BASIC PRUG POINTER.
33 HIMEM
         EQU
34 PPL
          EQU
35 PVL
          EGU
                           BASIC VAR POINTER.
                $E36B
$E51B
                             BASIC MEM FULL ERROR.
36 MEMFULL EQU
37 PRDEC EQU
                            BASIC DECIMAL PRINT SUBR.
38 RANGERR EQU $EE68
                             BASIC RANGE ERROR.
               $F0DF
$F689
39 LOAD
         EQU
                            BASIC LOAD SUBR.
                            SWFET 16 ENTRY.
40 SW16
          EQU
41 CROUT EQU
                $FD8E
                            CAR RET SUBR.
                            CHAR OUT SUBR.
42 COUT
          EQU
                $FDED
44 *
45 *
            SWEET 16 EQUATES
46 *
47 ACC
          EQU
                             SWEET 16 ACCUMULATOR.
48 NEWLOW EQU
                            NEW INITIAL LNO.
49 NEWINCR EQU
                $2
                             NEW LNO INCR.
                            LOW LND OF RENUM RANGE.
50 LNLOW EQU
                $3
                            HI LNO OF RENUM RANGE.
51 LNHI
                $4
         EQU
52 TBLSTRT EQU
                            LNO TABLE START.
53 TBLNDX1 EQU
                            PASS 1 LNO TBL INDEX.
54 TBLIM EQU
               $7
                            LNO TABLE LIMIT.
55 SCR8
          EQU
                $8
                            SCRATCH REG.
56 HMEM
          EQU
                $8
                            HIMEM (END OF PRGM).
57 SCR9
          EQU
                $9
                            SCRATCH REG.
58 PRGNDX EQU
                $9
                            PASS 1 PROG INDEX.
59 PRGNDX1 EQU $A
                            ALSO PROG INDEX.
60 NEWLN EQU
                            NEXT "NEW LNO"
                $B
                            PRIOR "NEW LNO" ASSIGN.
61 NEWLN1 EQU
                $C
                            PASS 2 LNO TABLE END.
62 TBLND EQU
                $6
63 PRGNDX2 EQU $7
                            PASS 2 PROG INDEX.
64 CHRO EQU $9
                            ASCII "O".
                            ASCII "A".
65 CHRA
         EQU
                $A
```

**** END 6502 CODE ****

Ар	ple 2 Technica	l Manual	Programmer's Aid # 11978
	122 *		
D452 2B	133 * 134 *	LD NEWLN	
D453 3C	135	ST NEWLN	
D154 A2	136	ADD NEWIN	
D455 3B	137	ST NEWLN	
D456 OD	138	HEX OD	'NUL' (WILL SKIP NEXT INSTRUCTION)
D457 D1	139 P1B	CPR NEWLO	
D458 02 C2	140	BNC PASSI	
D45A 00	141 RERR		"RANGE ERR" MESSAGE AND RETURN.
D45B 4C 68 (Ji1P RANGE	
D45E 00	143 MERR	RTN PRINT	"MEM FULL" MESSAGE AND RETURN.
D45F 4C 6B 1	E3 144	JMP MEMFU	LL
D462 EC	145 P1C	INR NEWLN	1 IF HI LNO <= MOST RECENT NEWLN THEN
D453 DC	146	CPR NENLN	1 RANGE ERROR.
D164 02 F4	147	BNC RERR	
	149 *		
	150 *	APPLE][BASIC RENUMBER / APPEND SUBROUTINE - PASS 2
	151 +		
D156 19 30 (SET CHRO,	\$00B0 ASCII "O".
0469 1A CO			\$00C0 ASCII "A".
D46C 27	154 P2A	LD PRGND	
D46D D8	155	CPR HMEM	IF PROG INDEX $=$ HIMEM THEN DONE PASS 2.
D46E 03 63	156	BC DONE	
0470 E7	157	INR PROND	
0471 67	158	LDD @PRGI	
0472 30	159 UPDATE		
0473 25	160	LD TBLST	
D474 3B D475 21	161	ST TBLND	
0476 1C	162 163	LD NEWLO	(WILL SKIP NEXT INSTR)
D477 20	164 UD2	LD NEWLN	
D478 A2	165	ADD NEWIN	
D479 3C	166	ST NEWLN	
D47A 23	167	LD TBLND	
D478 B6	168	SUB TBLND	
D47C 03 07	169	BC UD3	
D47E 6B	170	LDD @TBLN	DX2 NEXT LNO FROM TABLE.
D47F 3D	171	SUB OLDLA	
D480 07 F5	172	BNZ UD2	
D432 C7	173	POPD GERGN	DX2 REPLACE OLD LNO WITH CORRESPONDING
D483 2C	174	LD NEWLN	
D494 77	175	STD @PRGN	
D485 18 28			N, \$0028 STR CON TOKEN.
D488 1C	177	HEX 1C	(SKIPS NEXT TWO INSTRUCTIONS)
D489 67	178 GOTCON		- ·
D48A FC	179	DCR MODE	IF MODE = 0 THEN UPDATE LNO REF.
D488 08 E5	180	BM1 UPDAT	
0490 47 048E 09	181 ITEM 182	LD @PRGN CPR CHRO	DX2 BASIC TOKEN.
048E 02 09	193	BNG CHATC	K CHECK TOKEN FOR SPECIAL.
D491 DA	184	CPR CHRA	IF >= "O" AND < "A" THEN SKIP CONST
D492 02 F5	185	BINC GOTCO	
D494 F7	186 SKPASC		
D495 67	187	LDD @PRGN	
0196 05 FC	183	BM SKPAS	
0498 F7	189	DCR PRGNE	
D499 47	190	LO @PRGN	

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```
D49A DB
                 191 CHKTOK CPR
                                   STRCON
                                                STR CON TOKEN?
D49B 06 F7
                 192
                             ΒZ
                                   SKPASC
                                                 YES, SKIP SUBSEQUENT BYTES
D49D 1C 5D 00
                 193
                             SET
                                   REM, $005D
DIAO DC
                 194
                             CPR
                                   REM
                                                REM TOKEN?
D1A1 06 F1
                 195
                             ΒZ
                                   SXPASC
                                                 YES, SKIP SUBSEQUENT LINE.
D4A3 08 13
                 195
                             B/11
                                   CONTST
                                                GOSUB, LOOK FOR LINE NUMBER.
D4A5 FD
                 197
                             DCR
                                   R13
D1A6 FD
                 198
                             DCR
                                   R13
                                                (TOKEN $5F IS GOTO)
D4A7 06 OF
D4A9 1D 24 00
                 199
                             ΒZ
                                   CONTST
                 200
                             SET
                                   THEN, $0024
D4AC DD
                 201
                             CPR
                                   THEN
D4AD 06 09
                 505
                             ΒZ
                                   CONTST
                                                'THEN' LND, LOOK FOR LND.
DAAF FO
                 503
                             DCR
                                   ACC
D480 06 BA
                 204
                             ΒZ
                                   P2A
                                                EUL (TOKEN 01)?
D482 1D 74 00
                 205
                             SET
                                   LIST, $0074
D4B5 BD
                 206
                             SUB
                                   LIST
                                                SET MODELF LIST OR LIST COMMA.
0486 09 01
                 207
                             Bt4:11
                                   CONTS2
                                                 (TOKENS $74, $75)
D488 30
                 208 CONTST SUB
                                   ACC
                                                CLEAR MODE FOR LNO
D489 3C
                 209 CONTS2 ST
                                   HODE
                                                 UPDATE CHECK.
D4BA 01 D1
                 210
                            BR
                                   ITEM
                 212 *
                 213 *
                                APPLE IC BASIC APPEND SUBROUTINE
                 214 *
                 215 *
D48C 20 89 F6
                 216 APPEND USR
                                   SU16
D411F 1C 4E 00
                                   SCRC, HIMEM+2
                 217
                            SET
D402 CC
                 218
                            POPD
                                   QSCRC
                                                 SAVE HIMEM
D403 38
                 219
                            ST
                                   HMEM
D4C4 19 CA 00
                 220
                            SET
                                   SCR9, PPL
D4C7 69
                 221
                            LOD
                                   @SCR9
D4C8 7C
                 222
                            STD
                                   escrc
                                                SET HIMEM TO PRESERVE PROGRAM.
D4C9 00
                 223
                            RTN
D4CA 20 DF F0
                 224
                            JSR
                                   LOAD
                                                LOAD FROM TAPE
D4CD 20 89 F6
                 2:25
                            JSR
                                   SW16
D4DO CC
                            POPD
                                                RESTORE HIMEM TO SHOW BOTH PROGRAMS.
                 225
                                  @SCRC
D401 28
                 227
                            LD
                                   HMEM
                                                  (OLD AND NEW)
D4D2 7C
                 228
                            STD
                                   @SCRC
D4D3 00
                 229 DOME
                            RTN
                                   RETURN.
D4D4 60
                 230
                            RTS
```

--- END ASSEMBLY ----

TOTAL ERRORS: 00

: ASM

```
1 ********
2 *
        6502 RELOCATION
3 *
            SUBROUTINE
5 *
      1. DEFINE BLOCKS
          *A4<A1. A2 ^Y
7 *
          (^Y IS CTRL-Y)
8 *
9 *
      2. FIRST SEGMENT
10 *
          *A4CA1. A2 ^Y
11 *
12 *
             (IF CODE)
13 *
14 *
          *A4<A1. A2M
             (IF MOVE)
15 *
16 *
17 *
      3. SUBSEQUENT SEGMENTS
18 *
          *. A2 ^Y OR *. A2M
19 *
20 *
         WOZ 11-10-77
       APPLE COMPUTER INC.
21 *
22 *
23 ******************
25 *
26 *
        RELOCATION SUBROUTINE EQUATES
27 *
28 R1L
               $02 SWEET 16 REG 1.
               $08 3-BYTE INST FIELD.
$2F LENGTH CODE
29 INST
          EQU
30 LENGTH EQU
VARY 1E
          EQU
               $34 CMND BUF POINTER
32 A1L
          EQU
               #3C APPLE-II MON PARAM AREA.
33 A4L
          EGU
                $42 APPLE-II MON PARAM REG 4
34 IN
                $0200
          EQU
35 SW16
          EQU
               $F689 ; SWEET 16 ENTRY
              $F89E | DISASSEMBLER ENTRY
$FC84 POINTER INCR SUBR
36 INBD82 EQU
37 NXTA4 EQU
38 FRMBEQ EQU
                $01 SOURCE BLOCK BEGIN
39 FRMEND EQU
                $02 SOURCE BLOCK END
40 TOBEG EQU
                 $04 DEST BLOCK BEGIN
41 ADR
          EQU
                $06 ADR PART OF INST.
```

```
43 *
                  44 #
                         6502 RELOCATION SUBROUTINE
                  45 *
                             DRC
                  47
                             OBJ
                                   $A4DC
D4DC A4 34
                  48 RELOC LDY
                                   YSAV CMND BUF POINTER
D4DE B9 00 02
                  49
                            LDA
                                   IN, Y NEXT CMD CHAR
D4F1 C9 AA
                                   #$AA '#'?
RELOC2 NO, RELOC CODE SEG.
                  50
                             CMP
D4E3 DO OC
                  51
                             BNE
D4E5 E6 34
D4E7 A2 07
                  52
                             INC
                                   YSAV ADVANCE POINTER.
                             LDX
                  53
                                   #$07
D4E9 B5 3C
                            LDA
                  54 INIT
                                   A1L, X MOVE BLOCK PARAMS
D4EB 95 02
                  55
                             STA
                                   R1L, X
                                         FROM APPLE-II MON
D4ED CA
                  56
                             DEX
                                   AREA TO SW16 AREA
D4EE 10 F9
                  57
                             BPL
                                   INIT
                                         R1=SOURCE BEG, R2=
D4F0 60
                  58
                             RTS
                                   SOURCE END, R4=DEST BEG.
D4F1 A0 02
                  59 RELOC2 LDY
                                   #$02
D4F3 B1 3C
                  60 GETINS LDA
                                   (A1L), Y COPY 3 BYTES TO
D4F5 99 0B 00
D4F8 88
                  61
                            STA
                                   INST, Y
                                            SW16 AREA
                  62
                            DEY
D4F9 10 F8
                  63
                            BPL
                                   GETINS
D4FB 20 8E F8
D4FE A6 2F
                  64
                             JSR
                                   INSDS2 CALCULATE LENGTH OF
                  65
                            LDX
                                   LENGTH INST FROM OPCODE.
                                   0=1 BYTE, 1=2 BYTES,
D500 CA
                  66
                            DEX
D501 D0 OC
                  67
                            BNE
                                   XLATE 2=3 BYTES.
D503 A5 OB
                  68
                            LDA
                                   INST
D505 29 OD
                  69
                            AND
                                   ##OD WEED OUT NON-ZERO-PAGE
D507 FO 14
                  70
                                   STINST
                            BEQ
                                            2 BYTE INSTS (IMM).
D509 29 08
                  71
                             AND
                                   #$08
                                         IF ZERO PAGE ADR
                                   STINST THEN CLEAR HIGH BYTE
D50B D0 10
                  72
                            BNE
D50D 85 OD
                  73
                            STA
                                   INST+2
D50F 20 89 F6
                  74 XLATE JSR
                                   SW16 IF ADR OF ZERO PAGE
D512 22
                  75
                            LD
                                   FRMEND
                                           OR ABS IS IN SOURCE
D513 D6
                  76
                            CPR
                                        (FRM) BLOCK THEN
                                   ADR
D514 02 06
                  77
                            BNC
                                   SW16RT SUBSTITUTE
D516 26
                  78
                            LD
                                   ADR
                                        ADR-SOURCE BEG+DEST BEG
D517 B1
                  79
                            SUB
                                   FRMBEC
D518 02 02
                  80
                            BNC
                                   SW16RT
D51A A4
                  81
                            ADD
                                   TOBEC
D51B 36
                  82
                            ST
                                   ADR
D51C 00
                  83 SW16RT RTN
D51D A2 00
                  84 STINST LDX
                                   #$00
D51F B5 OB
                  85 STINS2 LDA
                                   INST, X
D521 91 42
                  86
                            STA
                                   (A4L), Y COPY LENGTH BYTES
D523 E8
                  87
                            INX
                                   OF INST FROM SW16 AREA TO
D524 20 B4 FC
                                   NXTA4
                  88
                            JSR
D527 C6 2F
                  89
                            DEC
                                   LENGTH
                                            DEST SEGMENT. UPDATE
D529 10 F4
                  90
                            BPL
                                   STINS2
                                            SOURCE, DEST SEGMENT
D52B 90 C4
                  91
                            BCC
                                                       LOOP IF NOT
                                   RELOC2
                                            POINTERS.
D52D 60
                  92
                            RTS
                                   BEYOND SOURCE SEC END.
```

--- END ASSEMBLY ---

TOTAL ERRORS: 00

```
2 *
                  3 *
                              TAPE VERIFY
                                JAN 78
                                BY WOZ
                  6
                  7
                  8
                    *****************
                 11 *
                 12 *
                           TAPE VERIFY EQUATES
                 13 *
                 14 CHKSUM EQU
                                  $2E
                 15 A1
                            EQU
                                  $3C
                                  $4C ; BASIC HIMEM POINTER
                 16 HIMEM
                            EQU
                 17 PP
                                  $CA ; BASIC BEGIN OF PROGRAM
                            EQU
                                  SCE ; BASIC PROGRAM LENGTH
                 18 PRLEN
                            EQU
                  19 XSAVE
                            EGU
                                  *D8 : PRESERVE X-REG FOR BASIC
                 20 HDRSET EQU
                                  $F11E ; SETS TAPE POINTERS TO $CE. CF
                 21 PROSET EQU
                                  $F12C ; SETS TAPE POINTERS FOR PROGRAM
                 22 NXTA1
                                  *FCBA ; INCREMENTS (A1) AND COMPARES TO (A2)
                            EQU
                 23 HEADR
                                  $FCC9
                            EQU
                  24 RDBYTE EQU
                                  $FCEC
                  25 RD2BIT EQU
                                  $FCFA
                                  $FCFD
                  26 RDBIT EGU
                  27 PRA1
                            EQU
                                  $FD92 ; PRINT (A1)-
                  28 PRBYTE EQU
                                  $FDDA
                  29 COUT
                            EQU
                                  $FDED
                  30 FINISH EQU
                                  $FF26 ; CHECK CHECKSUM, RING BELL
                  31 PRERR EQU
                                  $FF2D
                  33 *
                             TAPE VERIFY ROUTINE
                  34 *
                 35 *
                            ORG
                 36
                                  $D535
                 37
                                  $A535
                            OBJ
D535 86 D8
                  38 VFYBSC STX
                                  XSAVE ; PRESERVE X-REG FOR BASIC
D537 38
D538 A2 FF
                 39
                            SEC
                  40
                            LDX
                                  #$FF
D53A A5 4D
                  41 GETLEN LDA
                                  HIMEM+1 ; CALCULATE PROGRAM LENGTH
D53C F5 CB
                 42
                            SBC
                                  PP+1, X ; INTO PRLEN
D53E 95 CF
                 43
                            STA
                                  PRLEN+1, X
D540 E8
                  44
                            INX
D541 FO F7
                 45
                            BEQ
                                  GETLEN
D543 20 1E F1
                            JSR
                                  HDRSET ; SET UP POINTERS
                  46
D546 20 54 D5
                                  TAPEVFY ; DO A VERIFY ON HEADER
                  47
                            JSR
D549 A2 01
                  48
                            LDX
                                  ##01 ; PREPARE FOR PRGSET
D548 20 2C F1
                  49
                            JSR
                                  PRGSET ; SET POINTERS FOR PROGRAM VERIFY
D54E 20 54 D5
                  50
                            JSR
                                  TAPEVEY
D551 A6 D8
                  51
                            LDX
                                  XSAVE ; RESTORE X-REG
D553 60
                 52
                            RTS
```

```
53 *
                  54 * TAPE VERIFY RAM IMAGE (A1. A2)
                  55 *
D554 20 FA FC
                  56 TAPEVFY JSR
                                    RD2BIT
D557 A9 16
                  57
                             LDA
                                   #$16
D559 20 C9 FC
                  58
                             JSR
                                   HEADR ; SYNCHRONIZE ON HEADER
D55C 85 2E
                                   CHKSUM ; INITIALIZE CHKSUM
                  59
                             STA
D55E 20 FA FC
                  60
                             JSR
                                   RD2BIT
D561 AO 24
                  61 VRFY2 LDY
                                   #$24
D563 20 FD FC
                  62
                             JSR
                                   RDBIT
D566 BO F9
                  63
                             BCS
                                   VRFY2 ; CARRY SET IF READ A '1' BIT
D568 20 FD FC
                  64
                             JSR
                                   RDBIT
D568 AO 38
                  65
                             LDY
                                   #$3B
                  66 VRFY3
D56D 20 EC FC
                             JSR
                                   RDBYTE ; READ A BYTE
D570 FO 0E
                  67
                             BEQ
                                   EXTDEL ; ALWAYS TAKEN
D572 45 2E
                  68 VFYLOOP EOR
                                    CHKSUM ; UPDATE CHECKSUM
D574 85 2E
                  69
                             STA
                                   CHKSUM
D576 20 BA FC
                  70
                                   NXTA1 ; INCREMENT A1, SET CARRY IF A1>A2
                             JSR
D579 AO 34
                  71
                             LDY
                                   #$34 ; ONE LESS THAN USED IN READ FOR EXTRA 12
D578 90 F0
                  72
                             BCC
                                   VRFY3 ; LOOP UNTIL A1>A2
D57D 4C 26 FF
                  73
                             JMP
                                   FINISH ; VERIFY CHECKSUM&RING BELL
D580 EA
                  74 EXTDEL NOP
                                   FEXTRA DELAY TO EQUALIZE TIMING
D581 EA
                  75
                             NOP
                                        (+12 USEC)
D582 EA
                  76
                             NOP
D583 C1 3C
                  77
                             CMP
                                   (A1, X) ; BYTE THE SAME?
D585 FO EB
                  78
                             BEQ
                                   VFYLOOP ; IT MATCHES, LOOP BACK
D587 48
                                   SAVE WRONG BYTE FROM TAPE
                  79
                             PHA
D588 20 2D FF
                  80
                             JSR
                                   PRERR ; PRINT "ERR"
D58B 20 92 FD
                  81
                             JSR
                                   PRA1 ; DUTPUT (A1) "-"
D58E B1 3C
                  82
                            LDA
                                   (A1), Y
D590 20 DA FD
                  83
                                   PRBYTE ; OUTPUT CONTENTS OF A1
                             JSR
D593 A9 A0
                  84
                                   #$AO ; PRINT A BLANK
                            LDA
D595 20 ED FD
                  85
                             JSR
                                   COUT
D598 A9 A8
                  86
                            LDA
                                   #$AB ; '('
D59A 20 ED FD
                  87
                             JSR
                                   COUT
D59D 68
                  88
                                   DUTPUT BAD BYTE FROM TAPE
                            PLA
D59E 20 DA FD
                  89
                            JSR
                                   PRBYTE
D5A1 A9 A9
                  90
                            LDA
                                   #$A9 ;
D5A3 20 ED FD
                  91
                            JSR
                                   COUT
D5A6 A9 8D
                  92
                            LDA
                                   #$BD ; CARRIAGE RETURN, AND RETURN TO CALLER
D5A8 4C ED FD
                 93
                            JMP
                                   COUT
```

--- END ASSEMBLY ---

TOTAL ERRORS: 00

: ASM

```
3 *
          RAMTEST:
           BY WOZ
            6/77
      COPYRIGHT 1978 BY:
      APPLE COMPUTER INC
9 *
10 *
11 ******
13 *
14 *
          EQUATES:
15 *
                $0 TEST DATA $00 OR $FF
16 DATA
          EQU
17 NDATA
         EGU
                $1 INVERSE TEST DATA.
18 TESTD
                $2 GALLOP DATA.
         EQU
          EQU
                $6 AUX ADR POINTER.
19 R3L
20 R3H
          EQU
                $7
          EQU
                $8 AUX ADR POINTER.
21 R4L
          EQU
22 R4H
                $9
23 R5L
          EQU
                $A AUX ADR POINTER.
                $B
24 R5H
          EQU
25 R6L
          EQU
                $C GALLOP BIT MASK.
26 R6H
                $D ($0001 TO 2^N)
          EQU
                $34 MONITOR SCAN INDEX.
27 YSAV
          EQU
28 A1H
          EQU
                $3D BEGIN TEST BLOCK ADR.
29 A2L
                $3E LEN (PAGES) FROM MON.
          EQU
                *D5BO ; SET UP CNTRL-Y LOCATION
30 SETCTLY EQU
31 PRBYTE EQU
                $FDDA BYTE PRINT SUBR.
                SFDED CHAR OUT SUBR.
32 COUT
          EQU
                $FF2D PRINTS 'ERR-BELL'
33 PRERR EQU
34 BELL
          EQU
                $FF3A
```

```
36 *
                  37 *
                            RAMTEST:
                  38 *
                  39
                             ORG
                                    $D5BC
                  40
                             OBJ
                                    $A5BC
DSEC A9 C3
                                    ##C3 | SET UP CNTRL-Y LOCATION
                  41 SETUP
                             LDA
DSBE AO DS
                  42
                             LDY
                                    #$D5
D5CO 4C BO D5
                  43
                             JMP
                                    SETCTLY
                  44 RAMTST LDA
D5C3 A9 00
                                    #$0 TEST FOR $00,
D5C5 20 D0
                  45
                             JSR
                                    TEST
D5C8 A9 FF
                  46
                             LDA
                                    #$FF THEN $FF.
D5CA 20 D0 D5
                  47
                             JSR
                                    TEST
D5CD 4C
        3A FF
                  48
                             JMP
                                    BELL
D5D0 85 00
                  49 TEST
                             STA
                                    DATA
D5D2 49 FF
                  50
                             EOR
                                    #$FF
D5D4 85 01
                  51
                             STA
                                    NDATA
D5D6 A5 3D
                  52
                             LDA
                                    A1H
D5D8 85 07
                  53
                             STA
                                    RGH INIT (RGL, RGH),
D5DA 85 09
                  54
                             STA
                                    R4H
                                          (R4L, R4H), (R5L, R5H)
D5DC 85 OB
                  55
                             STA
                                    R5H
                                          TO TEST BLOCK BEGIN
D5DE A0 00
                  56
                             LDY
                                          ADDRESS.
                                    #$0
D5E0 84 06
                  57
                             STY
                                    R3L
D5E2 84 08
                  58
                             STY
                                    R4L
D5E4 84 0A
                  59
                             STY
                                    R5L
DSE6 A6
        3E
                  60
                             LDX
                                    A2L LENGTH (PAGES).
D5E8 A5 00
                  61
                             LDA
                                    DATA
D5EA 91 08
                  62 TESTO1 STA
                                    (R4L), Y SET ENTIRE TEST
DSEC CB
                  63
                             INY
                                    BLOCK TO DATA.
DSED DO FB
                  64
                             BNE
                                    TESTO1
D5EF E6 09
                  65
                             INC
                                    R4H
D5F1 CA
                  66
                             DFX
D5F2 D0 F6
                  67
                             BNE
                                    TEST01
D5F4 A6 3E
                  68
                             LDX
                                    A2L
DSF6 B1 06
                  69 TESTO2 LDA
                                    (R3L), Y VERIFY ENTIRE
D5F8 C5 00
                  70
                             CMP
                                    DATA
                                           TEST BLOCK.
D5FA FO 13
                  71
                                   TEST03
                             BEG
D5FC 48
                  72
                             PHA
                                   PRESERVE BAD DATA.
D5FD A5 07
                  73
                             LDA
D5FF 20 DA FD
                  74
                             JSR
                                   PRBYTE PRINT ADDRESS,
D602 98
                  75
                             TYA
D603 20 8A D6
                  76
                             JSR
                                   PRBYSP
D606 A5 00
                  77
                             LDA
                                   DATA THEN EXPECTED DATA,
                  78
79
D608 20 8A D6
                                   PRBYSP
                             JSR
84 E040
                             PLA
                                    THEN BAD DATA,
D60C 20 7F D6
                  80
                             JSR
                                   PRBYCR
                                            THEN 'ERR-BELL'.
DAOF CB
                  81 TESTOS INY
D610 D0 E4
                                   TEST02
                  82
                             BNE
D612 E6 07
                  83
                             INC
                                   R3H
D614 CA
                  84
                             DEX
D615 DO DF
                  85
                             BNE
                                   TEST02
D617 A6 3E
                  86
                             LDX
                                    AZL LENGTH.
D619 A5 01
                  87 TESTO4 LDA
                                   NDATA
D61B 91 OA
                  88
                             STA
                                    (R5L), Y SET TEST CELL TO
D61D 84 OD
                  89
                             STY
                                   RAH
                                          NDATA AND R6
D61F 84 OC
                  90
                             BTY
                                   R6L
                                          (GALLOP BIT MASK)
D621 E6 OC
                  91
                             INC
                                   R6L
                                          TO $0001.
D623 A5 01
                  92 TESTOS LDA
                                   NDATA
D625 20 45 D6
                  93
                             J8R
                                   TESTS GALLOP WITH NDATA.
D628 A5 00
                  94
                             LDA
                                   DATA
D62A 20 45 D6
                  95
                             JSR
                                   TEST6 THEN WITH DATA.
D95D 09 0C
                  96
                             ASL
                                   R6L
D62F 26 OD
                  97
                                   R6H SHIFT GALLOP BIT
                             ROL
D531 A5 OD
                  78
                             LDA
                                   R6H
                                          MASK FOR NEXT
```

```
D633 C5 3E
                                          NEIGHBOR.
                  99
                             CMP
                                                     DONE
                                   A2L
                                   TEST05
D635 90 EC
                 100
                             BCC
                                             IF > LENGTH.
D637 A5 00
                 101
                             LDA
                                   DATA
D639 91 0A
                                   (R5L), Y RESTORE TEST CELL.
                 102
                             STA
D63B E6 0A
                 103
                             INC
                                   R5L
D63D DO DA
                                   TEST04
                 104
                             BNE
D63F E6 0B
                                   R5H INCR TEST CELL
                 105
                             INC
D641 CA
                 106
                             DEX
                                   POINTER AND DECR
D642 D0 D5
                 107
                             BNE
                                   TESTO4
                                            LENGTH COUNT.
D644 60
                 108 RTS1
                             RTS
D645 85 02
                 109 TEST6
                             STA
                                   TESTD SAVE GALLOP DATA.
D647 A5 OA
                 110
                             LDA
                                   R5L
D649 45 OC
                 111
                             EOR
                                   R6L SET R4 TO R5
D648 85 08
                             STA
                                          EX-OR R6
                 112
                                   R4L
D64D A5 OB
D64F 45 OD
                 113
                             LDA
                                   R5H
                                          FOR NEIGHBOR
                 114
                             EOR
                                   R6H
                                          ADDRESS (1 BIT
D451 85 09
                 115
                                   R4H
                                          DIFFERENCE).
                             STA
D653 A5 02
                                    TESTD
                             LDA
                 116
                                    (R4L), Y GALLOP TEST DATA.
D655 91 08
                 117
                             STA
D557 B1 OA
                 118
                             LDA
                                    (R5L), Y CHECK TEST CELL
                                   NDATA
                                           FOR CHANGE.
D659 C5 01
                 119
                             CMP
D658 FO E7
                 120
                             BEQ
                                   RTS1 (OK).
                 121
                                   PRESERVE FAIL DATA.
D65D 48
                             PHA
D65E A5 OB
                 122
                             LDA
                                   R5H
D660 20 DA FD
                 123
                             JSR
                                   PRBYTE PRINT TEST CELL
                 124
D653 A5 OA
                             LDA
                                   R5L
                                         ADDRESS,
D655 20 8A D6
                                   PRBYSP
                 125
                             JSR
D668 A5 01
                 126
                             LDA
                                   NDATA
D55A 91 0A
                                    (R5L), Y (REPLACE CORRECT DATA)
                 127
                             STA
D65C 20 8A D6
                 128
                             JSR
                                   PRBYSP
                                             THEN TEST DATA BYTE,
D65F 68
                 129
                             PLA
D670 20 8A D6
                 130
                             JSR
                                   PRBYSP
                                             THEN FAIL DATA,
D673 A5 09
                 131
                             LDA
                                   R4H
D675 20 DA FD
                 132
                             JSR
                                   PRBYTE
D678 A5 08
                 133
                             LDA
                                    R4L
                                          THEN NEIGHBOR ADR.
                                   PRBYSP
D67A 20 8A D6
                             JSR
                 134
D67D A5 02
                 135
                             LDA
                                    TESTD
                                            THEN GALLOP DATA.
D67F 20 8A D6
                                    PRBYSP OUTPUT BYTE, SPACE.
                 136 PRBYCR JSR
                                            THEN 'ERR-BELL'.
D682 20 2D FF
                             JSR
                                    PRERR
                 137
D685 A9 8D
                 138
                                    ##8D ASCII CAR. RETURN.
                             LDA
D687 4C ED FD
                 139
                             JMP
                                    COUT
D68A 20 DA FD
                 140 PRBYSP JSR
                                    PRBYTE
                                    #$AO OUTPUT BYTE, THEN
D68D A9 A0
                 141
                             LDA
D68F 4C ED FD
                 142
                             JMP
                                    COUT
                                           SPACE.
                 143
                             ORG
                                    $3FB
03F8 4C C3 D5
                 144 USRLOC JMP
                                    RAMTST ENTRY FROM MON (CTRL-Y)
```

--- END ASSEMBLY ---

TOTAL ERRORS: 00

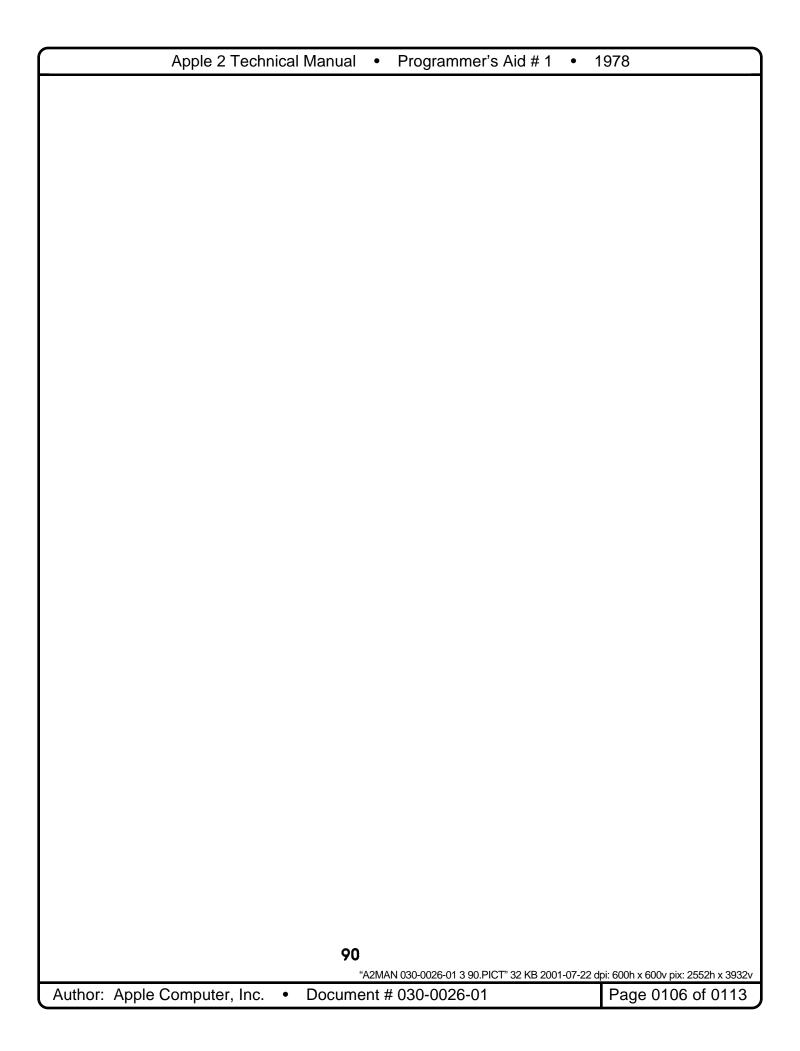
```
5 * MUSIC SUBROUTINE
                  7 * GARY J. SHANNON
                 ₽ ♦
                  *******************
                 10
                        ORG
                               $D717
                11 *
                 12 * ZERO PAGE WORK AREAS
                 13 * PARAMETER PASSING AREAS
                 14 *
                 15 DOWNTIME EQU
                 16 UPTIME EQU
                                $1
                 17 LENGTH EQU
                                $2
                 18 VOICE EQU
                                $2FD
                 19 LONG
                          EQU
                                $2FE
                 20 NOTE
                                $2FF
                          EQU
                 21 SPEAKER EQU
                                 $C030
D717 4C 4E D7
                22 ENTRY JMP
                                LOOKUP
                 23 *
                 24 * PLAY ONE NOTE
                 25 *
                 26 * DUTY CYCLE DATA IN 'UPTIME' AND
                 27 * 'DOWNTIME', DURATION IN 'LENGTH'
                 28 *
                 29 *
                 30 * CYCLE IS DIVIDED INTO 'UP' HALF
                 31 * AND 'DONN' HALF
                32 *
                               UPTIME ; GET POSITIVE PULSE WIDTH
                33 PLAY
D71A A4 01
                          LDY
D71C AD 30 CO
                34
                          LDA
                                SPEAKER : TOGGLE SPEAKER
D71F E6 02
                35 PLAY2 INC
                                LENGTH ; DURATION
D721 D0 05
                36
                           BNE
                                PATH1 ; NOT EXPIRED
D723 E6 03
                37
                           INC
                                LENGTH+1
D725 DO 05
                38
                          BNE
                                PATH2
D727 60
                                ; DURATION EXPIRED
                39
                          RTS
D728 EA
                 40 PATH1 NOP
                                ; DUMMY
D729 4C 2C D7
                 41
                           JMP
                                PATH2 ; TIME ADJUSTMENTS
D72C 88
                42 PATH2 DEY
                                ; DECREMENT WIDTH
D72D FO 05
                 43
                          BEQ
                                DOWN ; WIDTH EXPIRED
D72F 4C 32 D7
                 44
                          JMP
                                PATH3 ; IF NOT, USE UP
                 45 *
                 46 * DOWN HALF OF CYCLE
                 47 *
D732 DO EB
                 48 PATHS BNE
                                PLAY2 ; SAME # CYCLES
                                DOWNTIME : GET NEGATIVE PULSE WIDTH
D734 A4 00
                 49 DOWN
                          LDY
D736 AD 30 CO
                50
                          LDA
                                SPEAKER ; TOGGLE SPEAKER
                                LENGTH ; DURATION
D739 E6 02
                51 PLAY3 INC
D73B DO 05
                52
                           BNE
                                PATH4 ; NOT EXPIRED
D73D E6 03
                53
                          INC
                                LENGTH+1
D73F D0 05
                54
                          BNE
                                PATH5
D741 60
                          RTS
                                # DURATION EXPIRED
                55
D742 EA
                56 PATH4 NOP
                                ; DUMMY
D743 4C 46 D7
                                PATH5 ; TIME ADJUSTMENTS
                57
                          JITP
                               DECREMENT WIDTH
D746 88
                58 PATHS DEY
D747 FO D1
                59 BEQ
                               PLAY : BACK TO UP-SIDE
                                PATH6 : USE UP SOME CYCLES
D749 4C 4C D7
                60
                           JMP
D74C DO EB
                61 PATHS BNE
                                PLAY3 ; REPEAT
```

```
62 *
                 63 * NOTE TABLE LOOKUP SUBROUTINE
                 64 *
                 45 * GIVEN NOTE NUMBER IN 'NOTE'
                 66 * DURATION COUNT IN 'LONG'
                 67 # FIND 'UPTIME' AND 'DOWNTIME'
                 68 * ACCORDING TO DUTY CYCLE CALLED
                 69 * FOR BY 'VOICE'.
                 70 *
                                 NOTE ; GET NOTE NUMBER
D74E AD FF 02
                 71 LOOKUP LDA
                                 ; DOUBLE IT
                 72
                           ASL
D751 OA
                           TAY
D752 AB
                 73
D753 B9 96 D7
                           LDA
                                 NOTES, Y ; GET UPTIME
                                 DOWNTIME ; SAVE IT
                 75
                           STA
D756 85 00
                                 VOICE ; GET DUTY CYCLE
D758 AD FD 02
                 76
                           LDA
                 77 SHIFT LSR
D758 4A
                                 DONE ; SHIFT WIDTH COUNT
D75C FO 04
                 78
                           BEQ
                                  DOWNTIME ; ACCORDING TO VOICE
D75E 46 00
                 79
                           LSR
D760 D0 F9
                           BNE
                                  SHIFT
                 80
                                  NOTES, Y ; GET ORIGINAL
                 81 DONE
D762 B9 96 D7
                           LDA
                           SEC
D765 38
                 82
                                  DOWNTIME ; COMPUTE DIFFERENCE
D766 E5 00
                 83
                           SBC
D768 85 01
                 84
                           STA
                                  UPTIME ; SAVE IT
                                  ; NEXT ENTRY
                 85
                           INY:
D76A C8
                                  NOTES, Y ; GET DOWNTIME
D763 B9 96 D7
                 86
                           LDA
                           ADC
                                  DOWNTIME ; ADD DIFFERENCE
D76E 65 00
                 87
                                  DOWNTIME
                           STA
D770 85 00
                 88
D772 A9 00
                 89
                           LDA
                                  #0
D774 38
                 90
                           SEC
                                  LONG ; GET COMPLIMENT OF DURATION
D775 ED FE 02
                 91
                            SBC
                                  LENGTH+1 MOST SIGNIFICANT BYTE
D778 85 03
                 92
                            STA
D77A A9 00
                 93
                           LDA
                                  #0
                                  LENGTH
D77C 85 02
                 94
                            STA
                 95
                            LDA
                                  UPTIME
D77E A5 01
                 96
                                  PLAY ; IF NOT NOTE #0, PLAY IT
D780 D0 98
                           BNE
                 97 *
                 98 * 'REST' SUBROUTINE' PLAYS NOTE #0
                 99 * SILENTLY, FOR SAME DURATION AS
                 100 * A REGULAR NOTE.
                 101 *
                                 ; DUMMY
D782 EA
                 102 REST
                            NOP
                 103
                            NOP
                                  ; CYCLE USERS
D783 EA
D784 4C 87 D7
                            JMP
                                  REST2 ; TO ADJUST TIME
                104
                 105 REST2
                            INC
                                  LENGTH
D787 E6 02
D789 DO 05
                                  REST3
                106
                            BNE
                                  LENGTH+1
D78B E6 03
                107
                            INC
D78D D0 05
                 108
                            BNE
                                  REST4
                                  ; IF DURATION EXPIRED
D78F 60
                 109
                            RTS
                                  ; USE UP 'INC' CYCLES
                 110 REST3 NOP
D790 EA
D791 4C 94 D7
                            JMP
                                  REST4
                 111
                                  REST ; ALWAYS TAKEN
D794 DO EC
                112 REST4 BNE
```

```
113 *
                    114 * NOTE TABLES
                    115 *
D796 00 00 F6
                    116 NOTES HEX
                                         00, 00, F6, F6, E8, E8, DB, DB
D79E CF CF C3
                    117
                                  HEX
                                         CF, CF, C3, C3, B8, B8, AE, AE
D7A6 A4 A4 9B
                    118
                                         A4, A4, 98, 98, 92, 92, 8A, 8A
                                  HEX
D7AE 82 82 7B
                    119
                                  HEX
                                         82, 82, 73, 73, 74, 74, 6D, 6E
D786 67 68 61
D78E 52 52 4D
                    120
                                  HEX
                                         67, 68, 61, 62, 5C, 5C, 57, 57
                    121
                                  HEX
                                         52, 52, 4D, 4E, 49, 49, 45, 45
D7C6 41 41 3D
                    122
                                         41, 41, 3D, 3E, 3A, 3A, 36, 37
                                  HEX
D7CE 33 34 30
D7D6 29 29 26
                    123
                                  HEX
                                         33, 34, 30, 31, 2E, 2E, 2B, 2C
                                         29, 29, 26, 27, 24, 25, 22, 23
                    124
                                  HEX
D7DE 20 21 1E
                    125
                                  HEX
                                         20, 21, 1E, 1F, 1D, 1D, 1B, 1C
D7E6 1A 1A 18
D7EE 14 15 13
                    126
                                  HEX
                                         1A, 1A, 1B, 19, 17, 17, 15, 16
                                         14, 15, 13, 14, 12, 12, 11, 11
                    127
                                  HEX
D7F6 10 10 OF
                    128
                                 HEX
                                         10, 10, OF, 10, OE, OF
```

--- END ASSEMBLY ---

TOTAL ERRORS: 00



APPENDIX SUMMARY OF PROGRAMMER'S AID COMMANDS

- 92 Renumber
- 92 Append
- 92 Tape Verify (BASIC)
- 93 Tape Verity (Machine Code and Data)
- 93 Relocate (Machine Code and Data)
- 94 RAM Test
- 94 Music
- 95 High-Resolution Graphics
- 96 Quick Reference to High-Resolution Graphics Information

Chapter 1: RENUMBER

(a) To renumber an entire BASIC program:

CLR START = 1000STEP = 10CALL -10531

(b) To renumber a program portion:

CLR START = 200STEP = 20FROM = 300 (program portion TO = 500 to be renumbered) CALL -10521

Chapter 2: APPEND

(a) Load the second BASIC program, with high line numbers:

LOAD

(b) Load and append the first BASIC program, with low line numbers:

CALL -11076

Chapter 3: TAPE VERIFY (BASIC)

(a) Save current BASIC program on tape:

SAVE

(b) Replay the tape, after:

CALL -1Ø955

Chapter 4: TAPE VERIFY (Machine Code and Data)

(a) From the Monitor, save the portion of memory on tape:

address1 . address2 W return

(b) Initialize Tape Verify feature:

D52EG return

(c) Replay the tape, after:

address1 . address2 ctrl Y return

Note: spaces shown within the above commands are for easier reading only; they should not be typed.

Chapter 5: RELOCATE (Machine Code and Data)

(a) From the Monitor, initialize Code-Relocation feature:

D4D5G return

(b) Blocks are memory locations from which program runs. Specify Destination and Source Block parameters:

Dest Blk Beg < Source Blk Beg . Source Blk End ctrl Y * return

(c) Segments are memory locations where parts of program reside. If first program Segment is code, Relocate:

Dest Seg Beg < Source Seg Beg . Source Seg End ctrl Y return

If first program Segment is data, Move:

Dest Seg Beg < Source Seg Beg . Source Seg End return

- (d) In order of increasing address, Move subsequent contiguous data Segments:
 - Source Segment End ctrl Y return

and Relocate subsequent contiguous code Segments:

Source Segment End M return

Note: spaces shown within the above commands are for easier reading only; they should <u>not</u> be typed.

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Chapter 6: RAM TEST

(a) From the Monitor, initialize RAM Test program:

D5BCG return

(b) To test a portion of memory:

address · pages ctrl Y return (test begins at address, continues for length pages.

Note: test length, pages*100, must <u>not</u> be greater than starting address. One page = 256 bytes (\$100 bytes, in Hex).

(c) To test more memory, do individual tests or concatenate:

addrl.pages1 ctrl Y addr2.pages2 ctrl Y addr3.pages3 ctrl Y return

Example, for a 48K system:

400.4 ctrl Y 800.8 ctrl Y 1000.10 ctrl Y 2000.20 ctrl Y 3000.20 ctrl Y 4000.40 ctrl Y 7000.20 ctrl Y 8000.40 ctrl Y return

(d) To repeat test indefinitely:

N complete test 34:0 type one space return

Note: except where specified in step (d), spaces shown within the above commands are for easier reading only; they should <u>not</u> be typed.

Chapter 7: MUSIC

(a) Assign appropriate variable names to CALL and POKE locations (optional):

MUSIC = -10473 PITCH = 767 TIME = 766 TIMBRE = 765

(b) Set parameters for next note:

POKE PITCH, p
POKE TIME, m
POKE TIMBRE, t

(p = 1 to 50; 32 = middle C)
(m = 1 to 255; 170 = 1 second)
(t = 2, 8, 16, 32 or 64)

(c) Sound the note:

CALL MUSIC

Chapter 8: HIGH-RESOLUTION GRAPHICS

- (a) Set order of parameters (first lines of program):
 - 1 $X\emptyset = Y\emptyset = COLR$ 2 SHAPE = ROT = SCALE (if shapes are used)
- (b) Assign appropriate variable names to subroutine calling addresses (optional; omit any subroutines not used in program):

```
10 INIT = -12288 : CLEAR = -12274 : BKGND = -11471
11 POSN = -11527 : PLOT = -11506 : LINE = -11500
12 DRAW = -11465 : DRAW1 = -11462
13 FIND = -11780 : SHLOAD = -11335
```

(c) Assign appropriate variable names to color values (optional; omit any colors not used in program):

```
2Ø BLACK = Ø: LET GREEN = 42: VIOLET = 85
21 WHITE = 127: ORANGE = 17Ø: BLUE = 213
22 BLACK2 = 128: WHITE2 = 255
```

(d) Initialize:

30 CALL INIT

(e) Change screen conditions, if desired. Set appropriate parameter values, and CALL desired subroutines by name.

Example:

```
40 COLR = VIOLET : CALL BKGND : REM TURN BACKGROUND VIOLET
50 FOR I = 0 TO 279 STEP 5
60 X0 = 140 : Y0 = 150 : COLR = WHITE : REM SET PARAMETERS
70 CALL POSN : REM MARK THE "CENTER"
80 X0 = I : Y0 = 0 : REM SET NEW PARAMETERS
90 CALL LINE : REM DRAW LINE TO EDGE
100 NEXT I : END
```

QUICK REFERENCE TO HIGH-RESOLUTION INFORMATION

Subroutine <u>Name</u>	CALLing Address	Parameters <u>Needed</u>
INIT	-12288	
CLEAR	- 12274	
BKGND	-11471	COLR .
POSN	-11527	XØ, YØ, COLR
PLOT	-115 Ø 6	XØ, YØ, COLR
LINE	-115ØØ	XØ, YØ, COLR
DRAW	-11465	XØ, YØ, COLR, SHAPE, ROT, SCALE
DRAW1	-11462	SHAPE, ROT, SCALE
FIND	-1178Ø	
SHLOAD	-11335	

Color	COLR	Color	COLR
Name	<u>Value</u>	Name	<u>Value</u>
BLACK	Ø	BLACK2	128
GREEN	42	ORANGE	17Ø
VIOLET	85	BLUE	213
WHITE	127	WHITE2	255

(Note: on systems below S/N 6000, colors in the second column appear identical to those in the first column)

CHANGING THE HIGH-RESOLUTION GRAPHICS DISPLAY

Full-Screen Graphics	POKE −163Ø2, Ø	
Mixed Graphics-Plus-Text (Default)	POKE −163Ø1, Ø	
Page 2 Display	POKE −16299, Ø	
Page 1 Display (Normal)	POKE -163∅Ø, Ø	
Page 2 Plotting	POKE 8Ø6, 64	
Page 1 Plotting (Default)	POKE 8Ø6, 32	

(Note: CALL INIT sets mixed graphics-plus-text, and Page 1 plotting, but does not reset to Page 1 <u>display</u>.)

Collision Count for Shapes PEEK (810)

(Note: the change in PEEKed value indicates collision.)

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